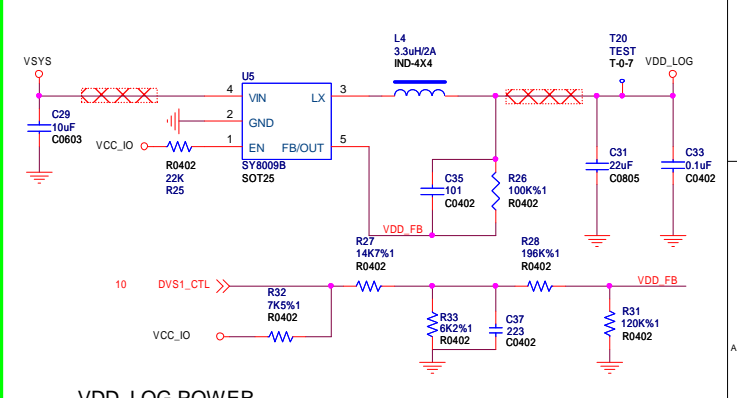
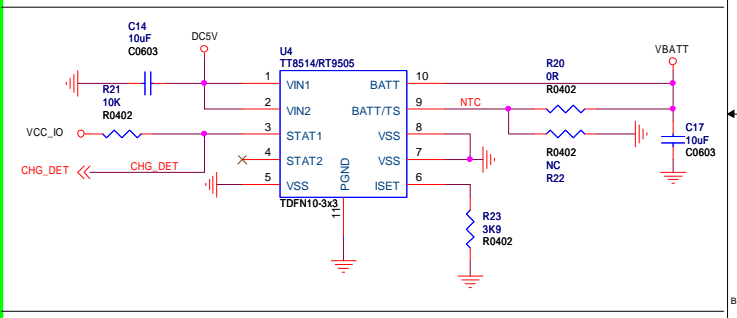
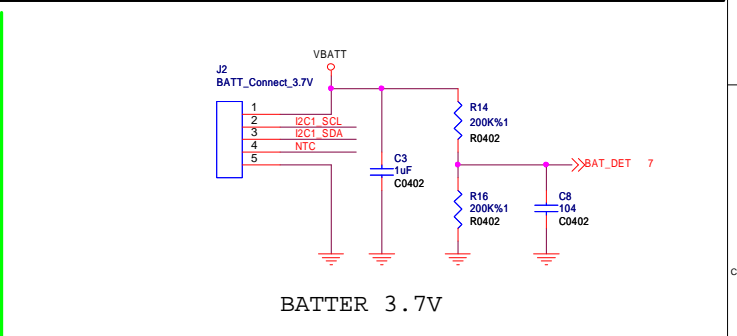
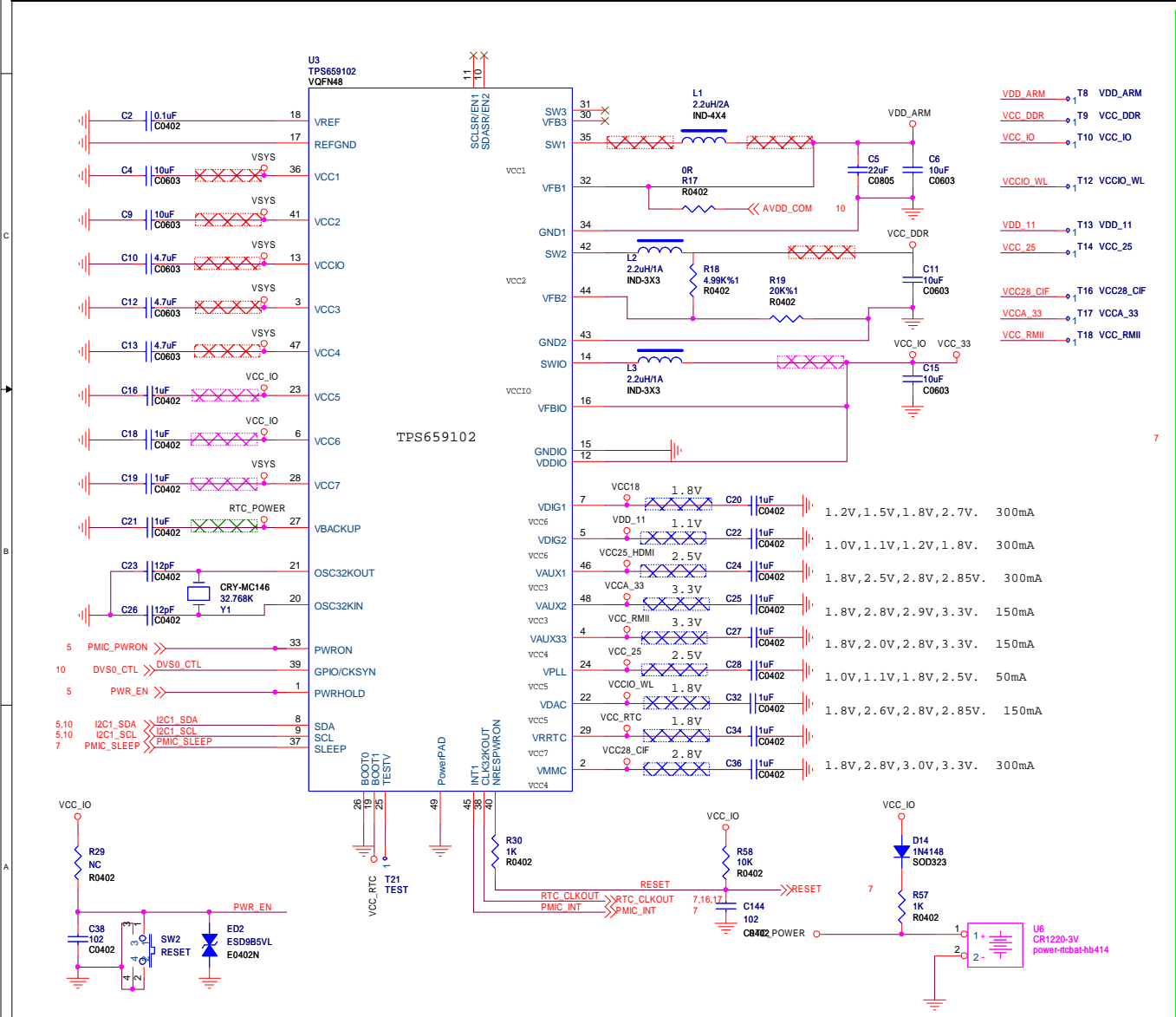
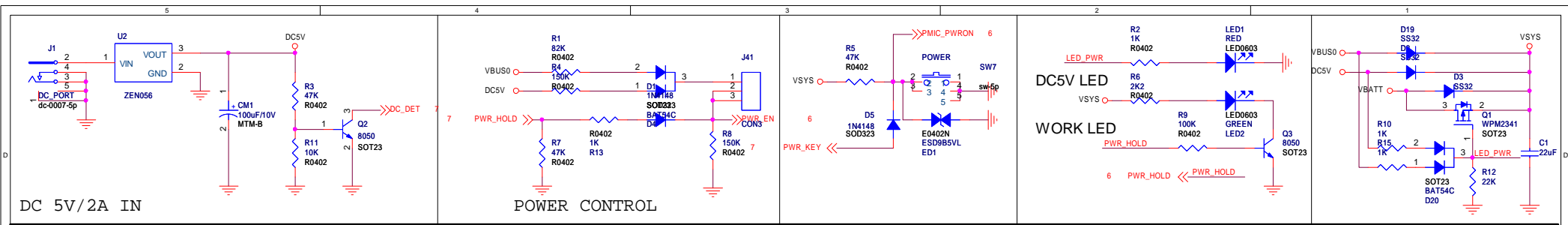


POWER DIAGRAM



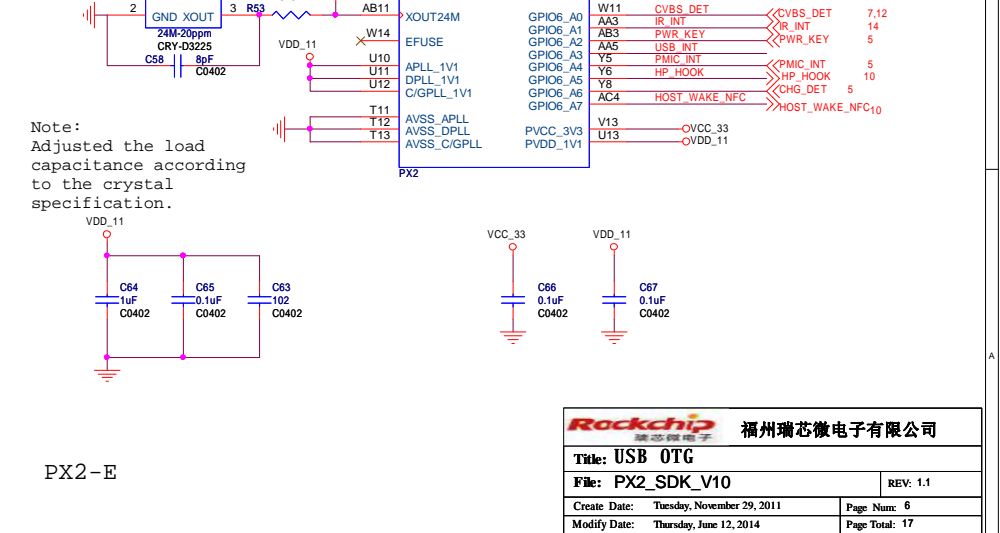
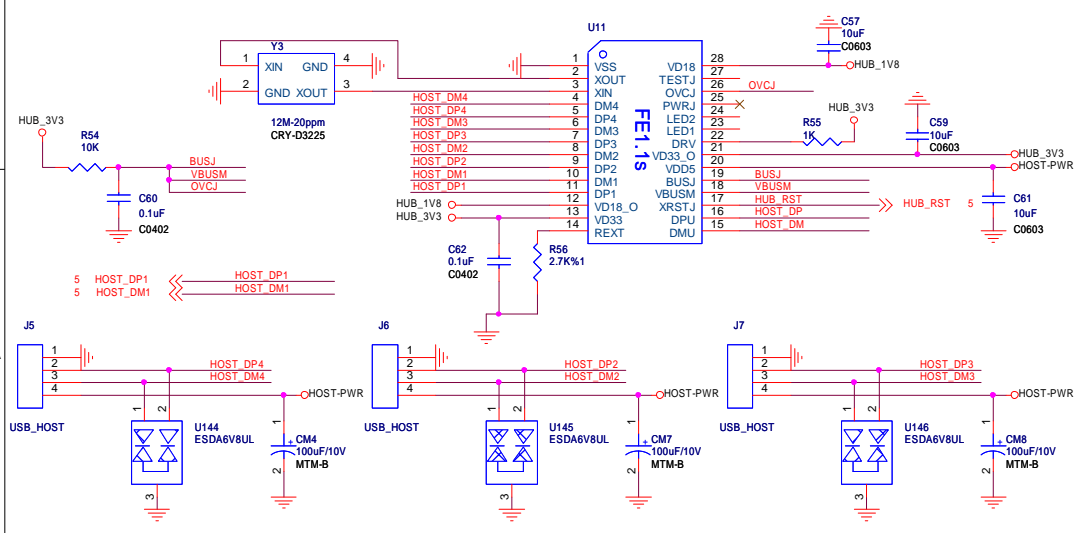
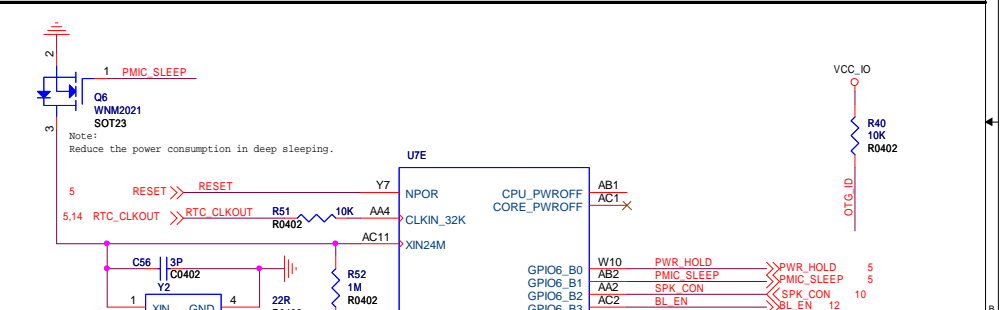
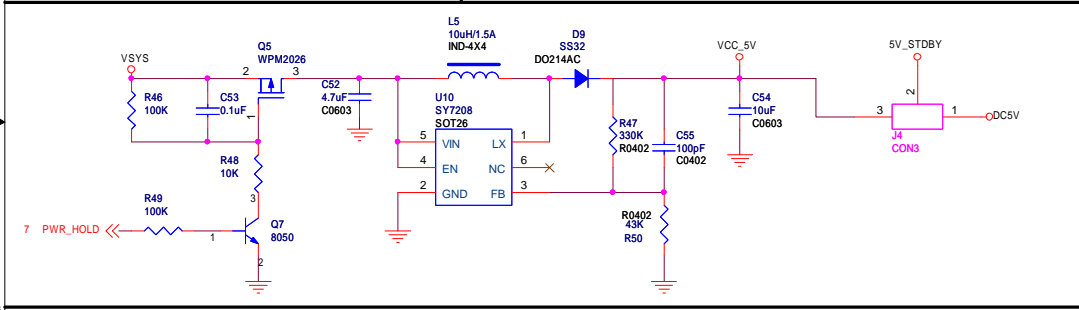
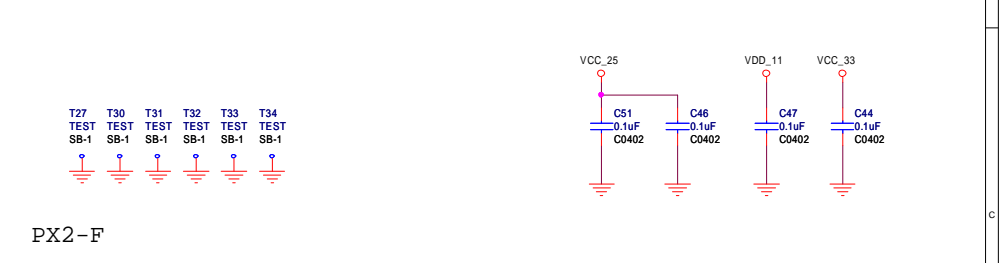
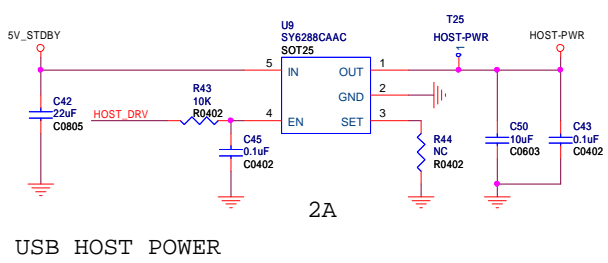
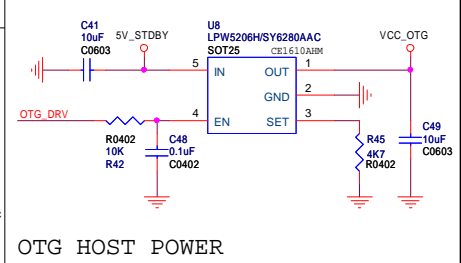
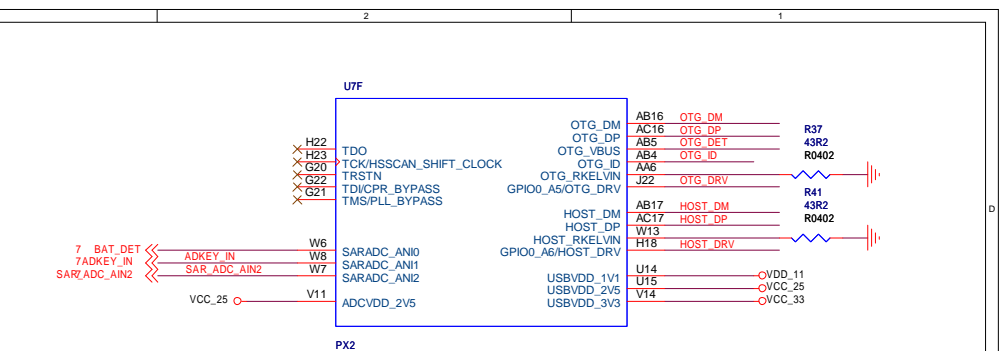
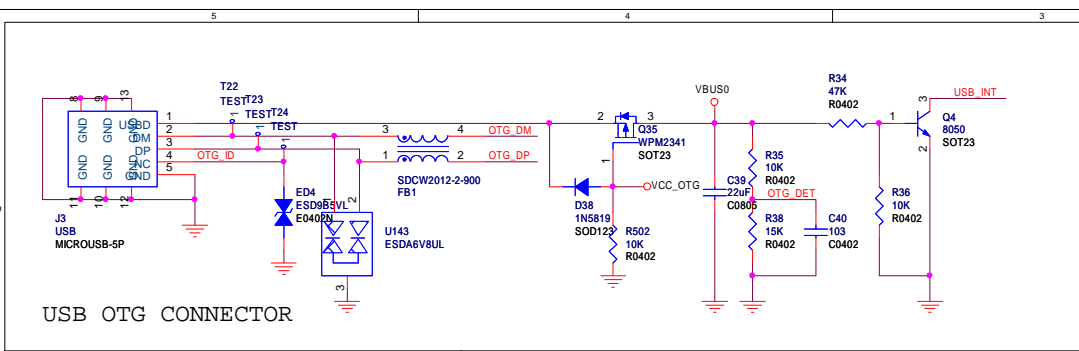
Rackchip 福州瑞芯微电子有限公司

Title: System power

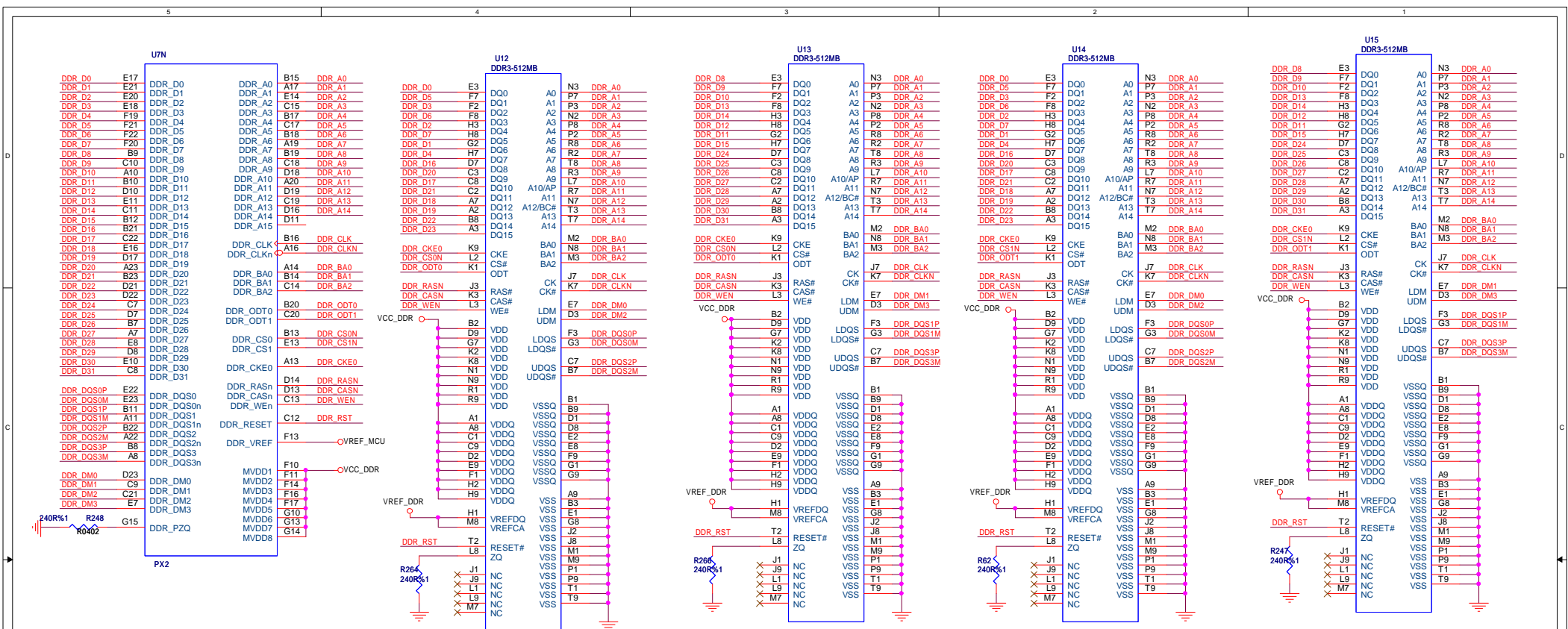
File: PX2_SDK_V10 REV: 1.1

Create Date: Tuesday, November 29, 2011 Page Num: 5

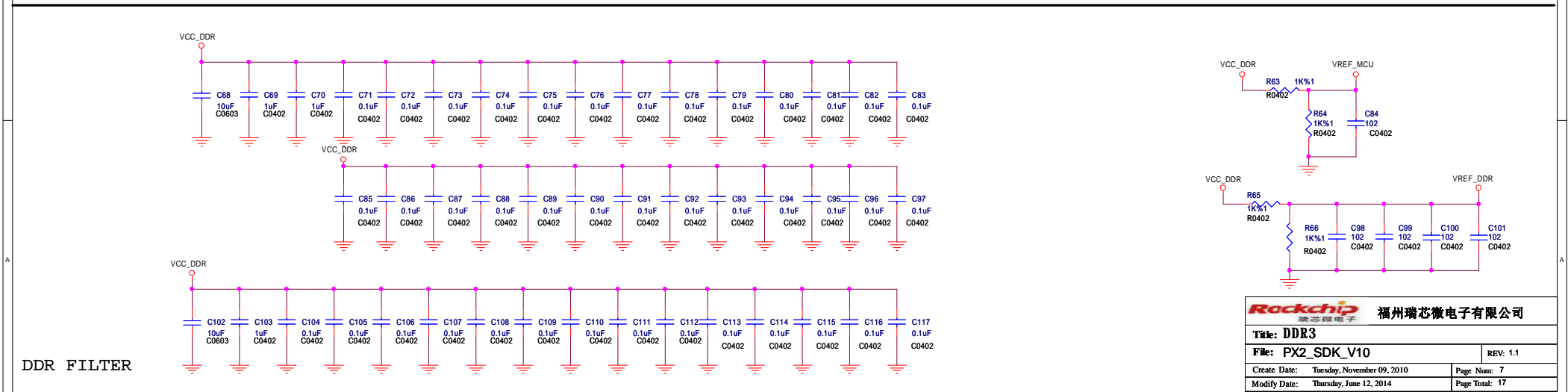
Modify Date: Thursday, June 12, 2014 Page Total: 17



Rackchip 福州瑞芯微电子有限公司	
Title: USB_OTG	
File: PX2_SDK_V10	
Create Date: Tuesday, November 29, 2011	Page Name: 6
Modify Date: Thursday, June 12, 2014	Page Total: 17



PX2-N



DDR FILTER

Rockchip
福州瑞芯微电子有限公司

Title: DDR3

File: PX2_SDK_V10

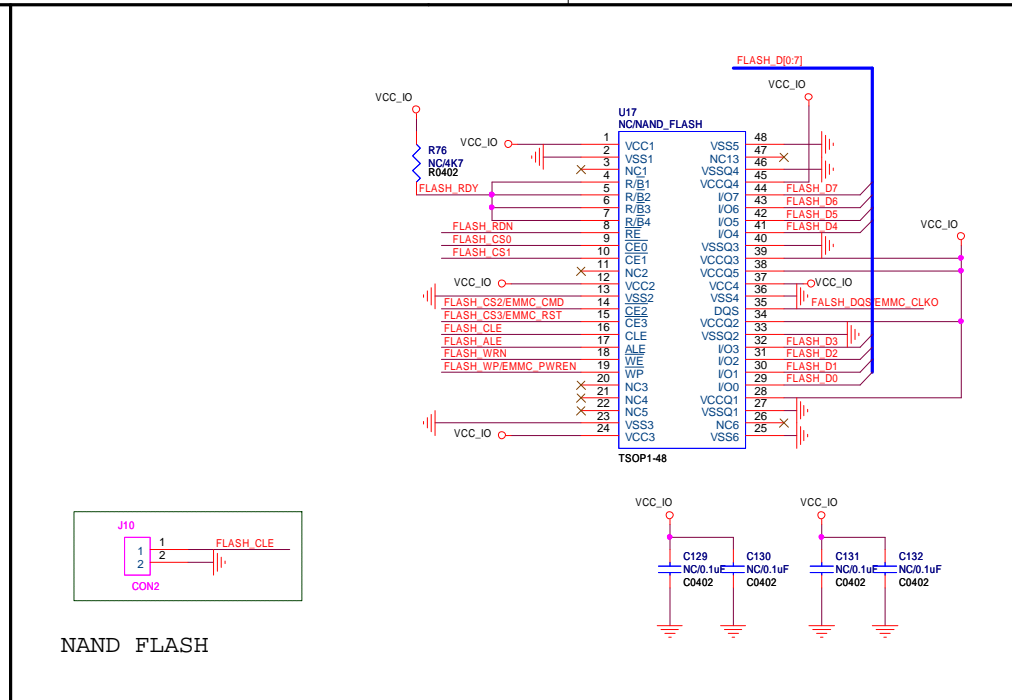
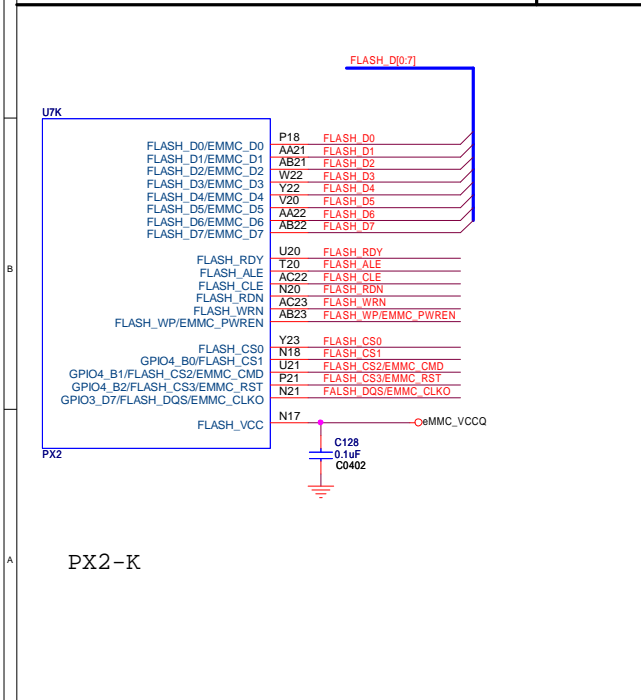
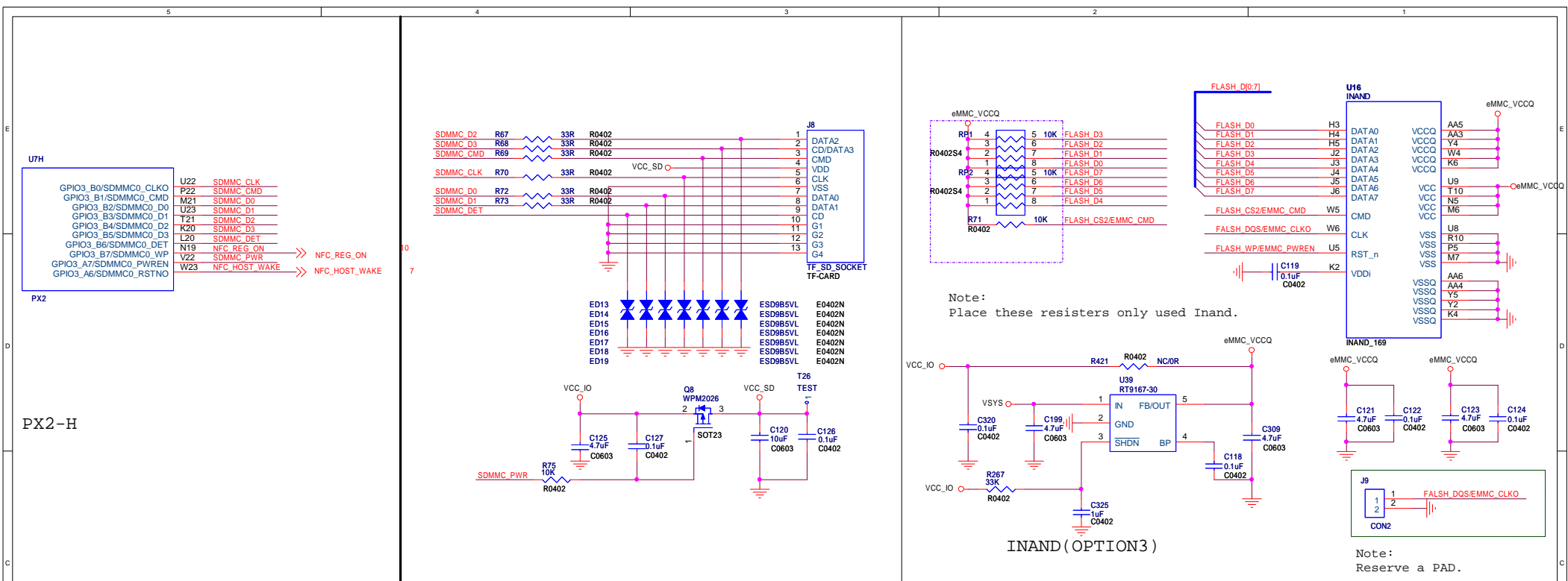
REV: 1.1

Create Date: Tuesday, November 09, 2010

Page Num: 7

Modify Date: Thursday, June 12, 2014

Page Total: 17



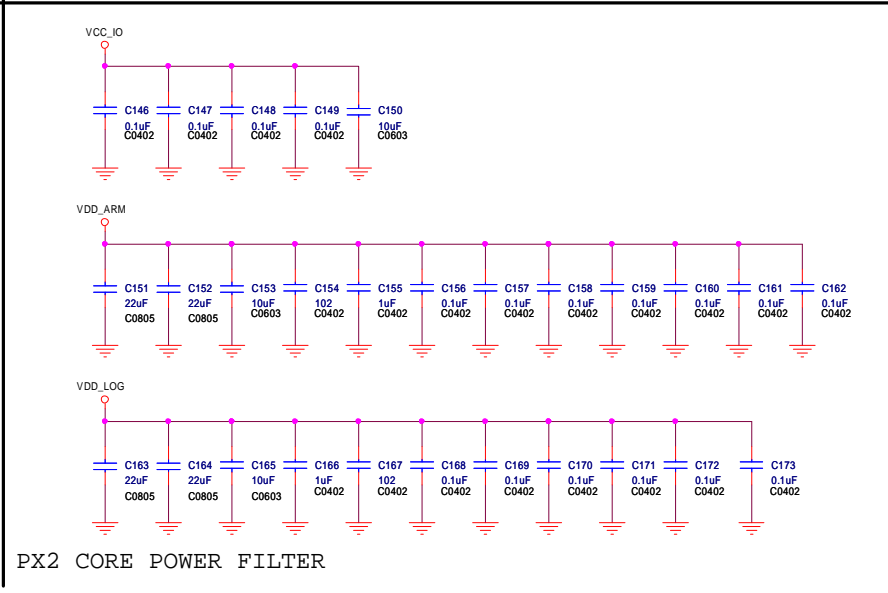
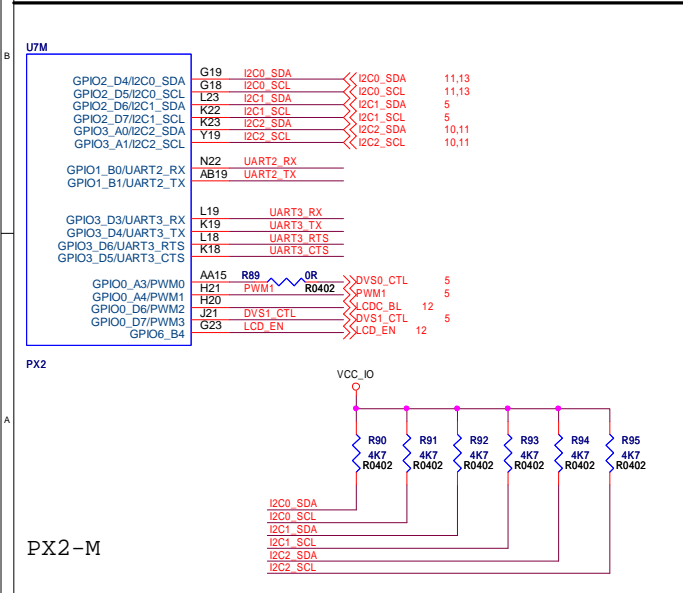
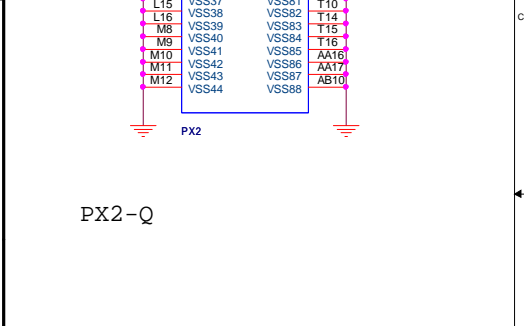
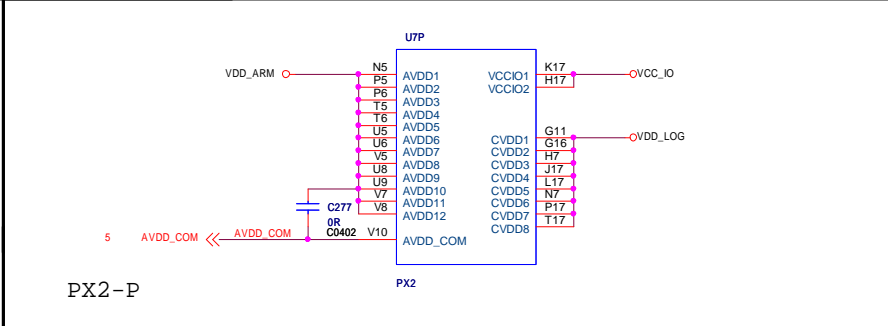
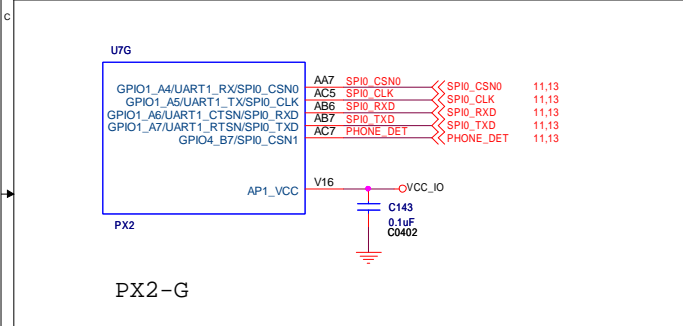
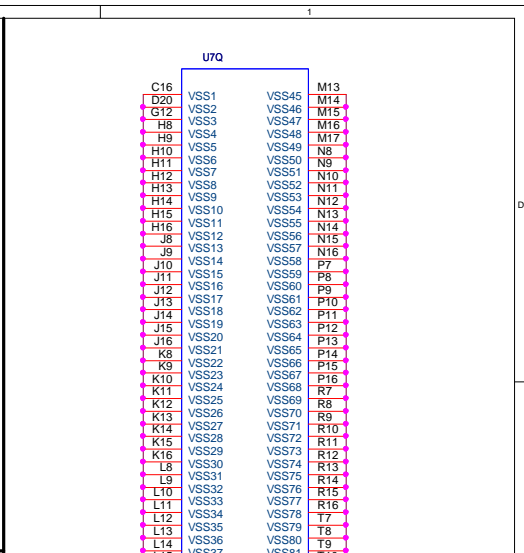
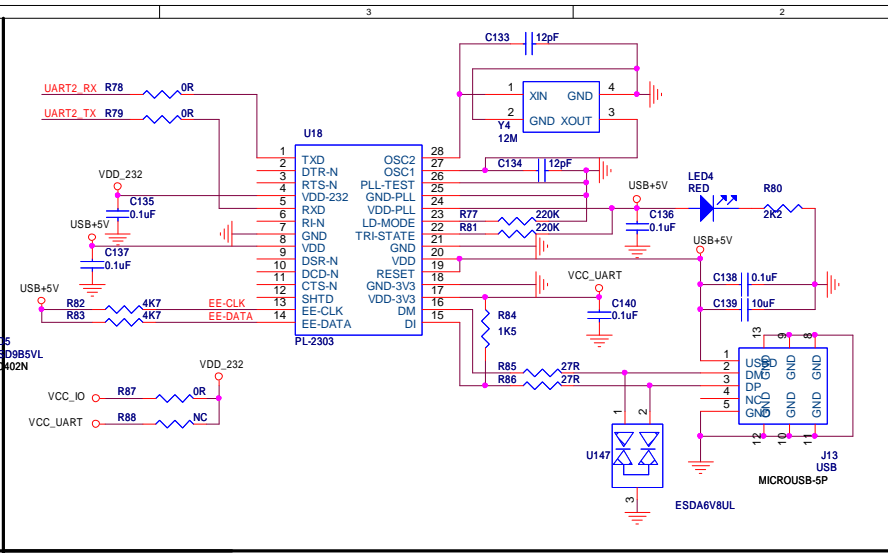
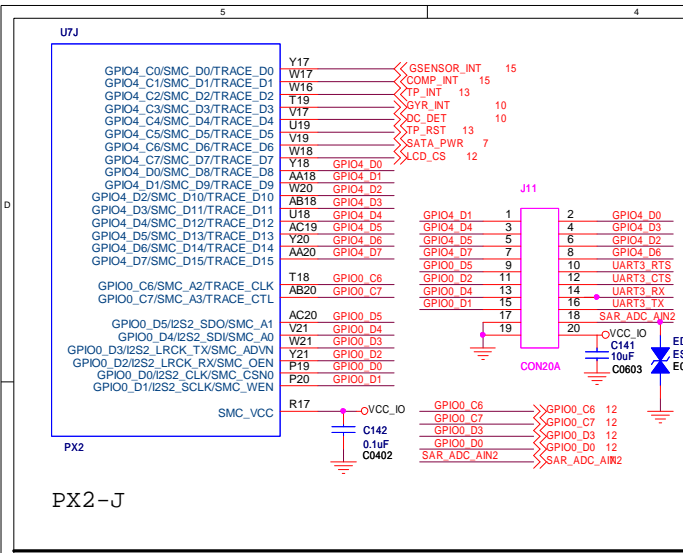
Rockchip
福州瑞芯微电子有限公司

Title: Nand Flash/SD

File: PX2_SDK_V10 REV: 1.1

Create Date: Thursday, October 14, 2010 Page Name: 8

Modify Date: Thursday, June 12, 2014 Page Total: 17



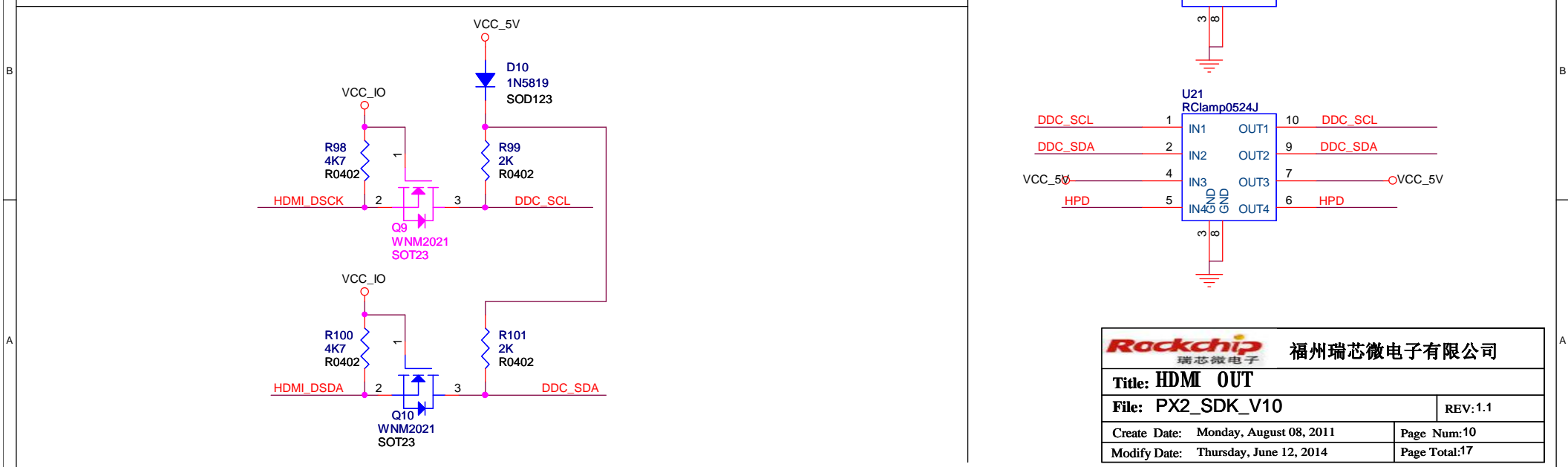
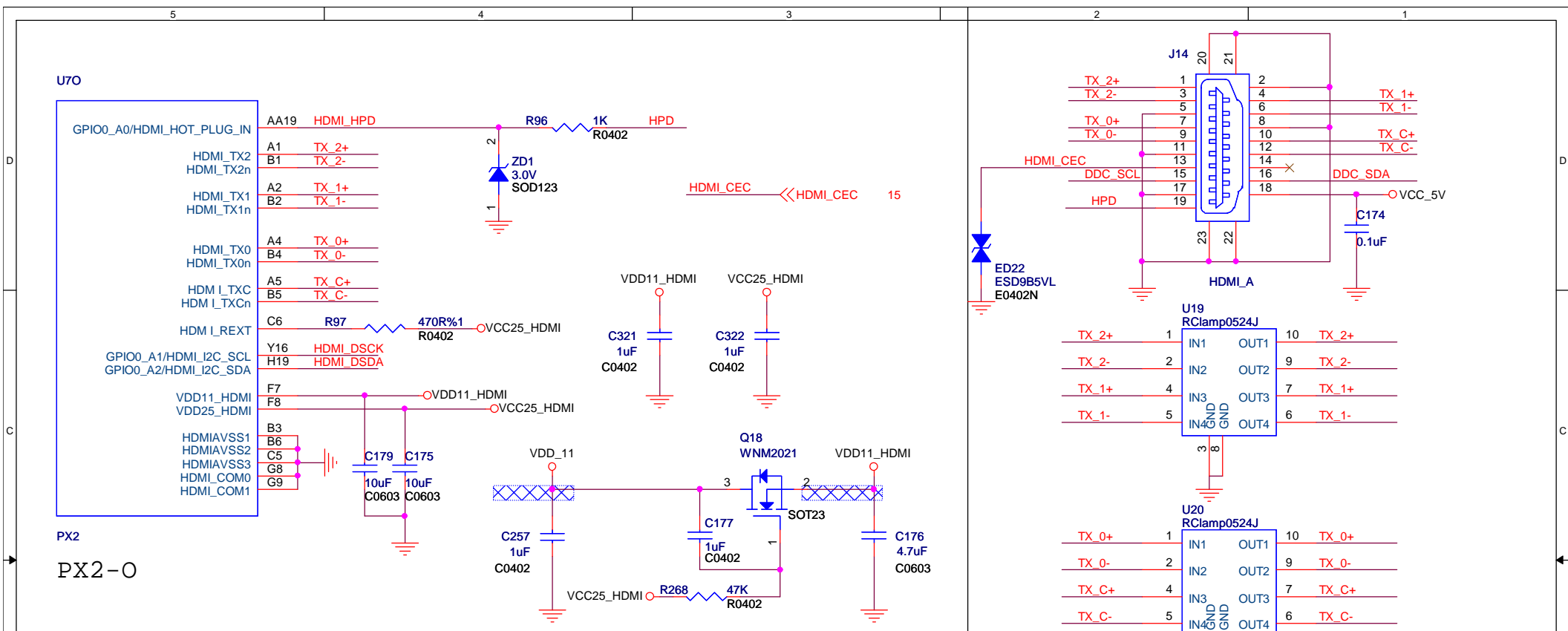
Rockchip 福州瑞芯微电子有限公司

Title: GPIO/RK3066 POWER

File: PX2_SDK_V10 REV: 1.1

Create Date: Monday, October 18, 2010 Page Num: 9

Modify Date: Thursday, June 12, 2014 Page Total: 17



Rockchip 瑞芯微电子 福州瑞芯微电子有限公司

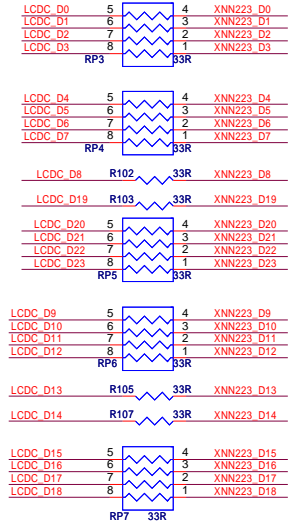
Title: HDMI OUT

File: PX2_SDK_V10 REV:1.1

Create Date: Monday, August 08, 2011 Page Num:10

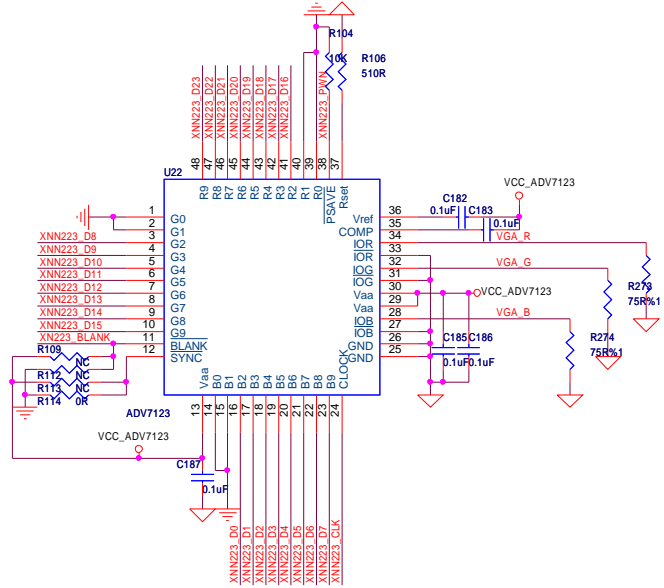
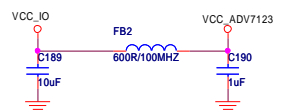
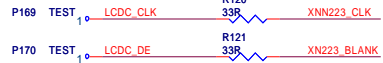
Modify Date: Thursday, June 12, 2014 Page Total:17

- U7A
- LCDO_DEN
 - LCDO_DCLK
 - LCDO_VSYNC
 - LCDO_HSYNC
 - LCDO_D0
 - LCDO_D1
 - LCDO_D2
 - LCDO_D3
 - LCDO_D4
 - LCDO_D5
 - LCDO_D6
 - LCDO_D7
 - LCDO_D8
 - LCDO_D9
 - LCDO_D10
 - LCDO_D11
 - LCDO_D12
 - LCDO_D13
 - LCDO_D14
 - LCDO_D15
 - LCDO_D16
 - LCDO_D17
 - LCDO_D18
 - LCDO_D19
 - LCDO_D20
 - LCDO_D21
 - LCDO_D22
 - LCDO_D23
 - LCDO_VCC0
 - LCDO_VCC1

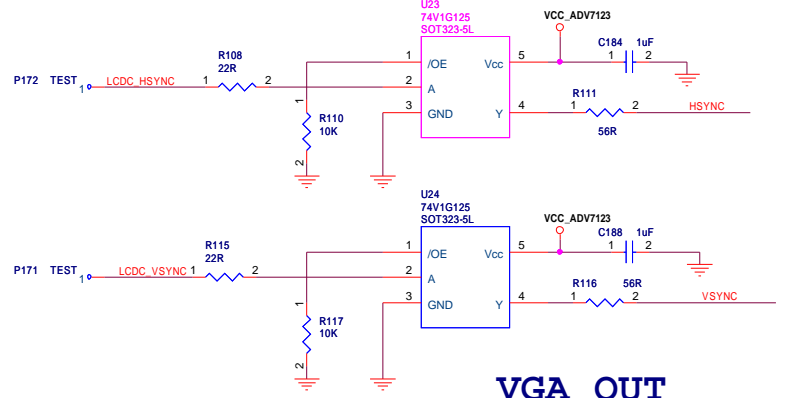
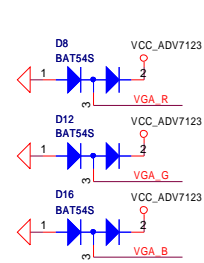
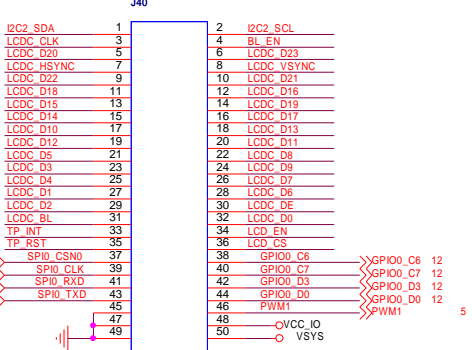
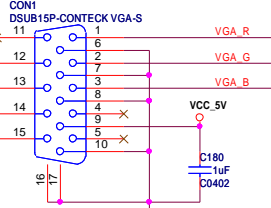


PX2-A

- XNN223_PWN
- I2C2_SDA
- I2C2_SCL
- LCD_CS
- LCD_EN
- TP_RST
- TP_INT
- LCDC_BL
- BL_EN
- XNN223_PWN
- I2C2_SDA
- I2C2_SCL
- LCD_CS
- LCD_EN
- TP_RST
- TP_INT
- LCDC_BL
- BL_EN

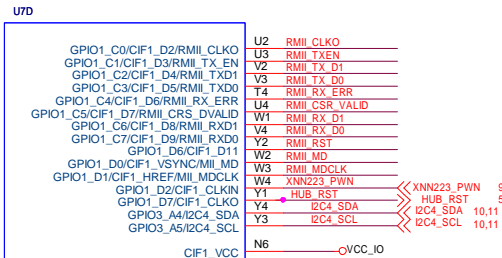


VGA OUT

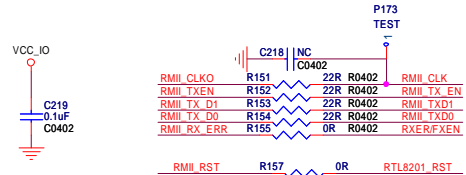


VGA OUT

Rackchip 福州瑞芯微电子有限公司	
Title: YPbPr/CVBS/VGA OUT	
File: PX2_SDK_V10	REV: 1.1
Create Date: Friday, October 15, 2010	Page Num: 11
Modify Date: Thursday, June 12, 2014	Page Total: 17



PX2-D

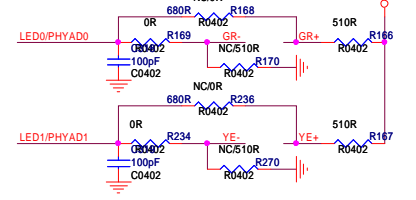


RMII REF_CLK direction

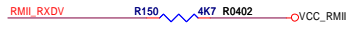


Pull Low for RMII REF_CLK Output mode
Pull High for RMII REF_CLK Input mode(default)

PHY Address/LED



MII/RMII Selection



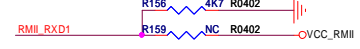
Pull High for RMII mode(default)

UTP / Fiber Selection

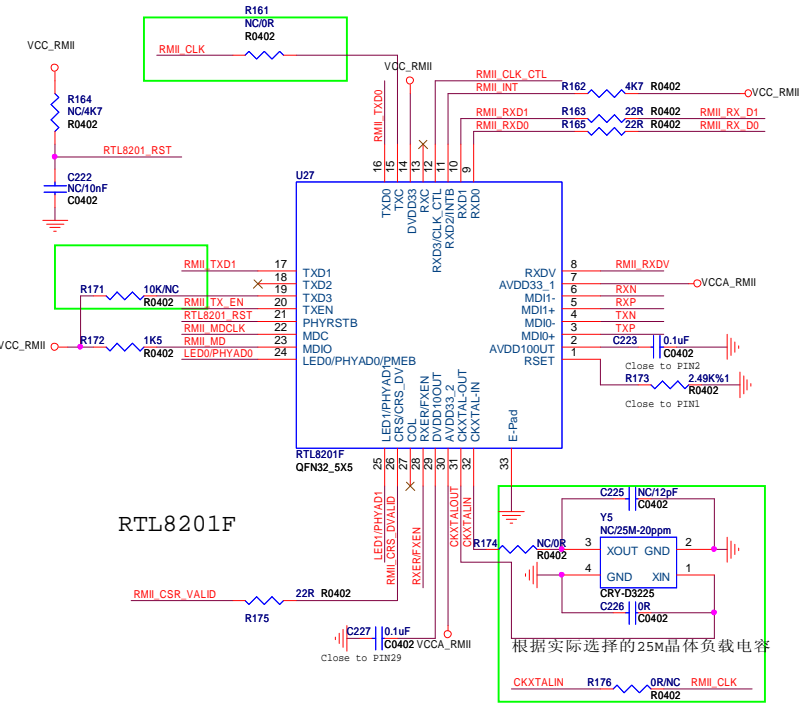


Pull Low for UTP Mode(default)
Pull High for Fiber Mode

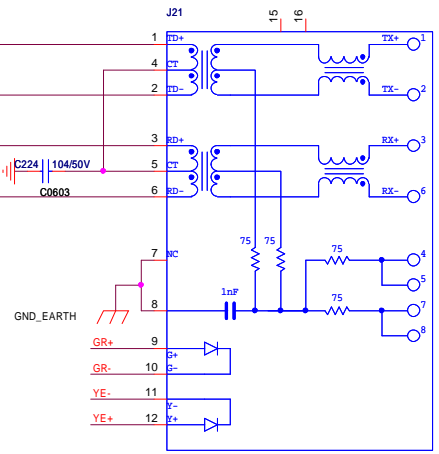
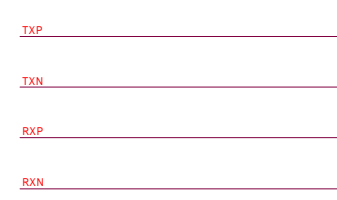
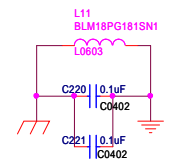
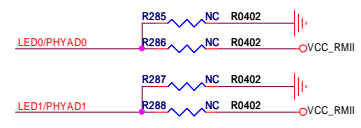
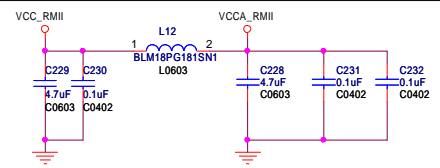
WOL/LED0 Selection



Pull Low for LED0 Mode(default)
Pull High for WOL Mode
WOL:Wake-on-LAN

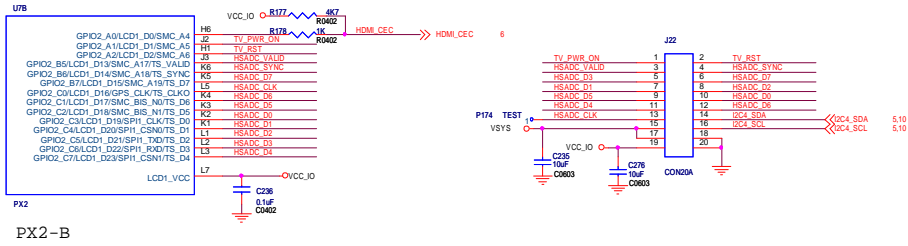


RTL8201F POWER

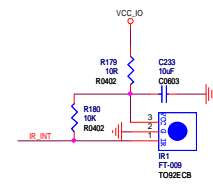


HR91105A

Rackchip 福州瑞芯微电子股份有限公司	
Title: RMII Ethernet	
File: PX2_SDK_V10	
REV: 1.1	
Create Date: Friday, October 15, 2010	Page Num: 13
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PX2-B

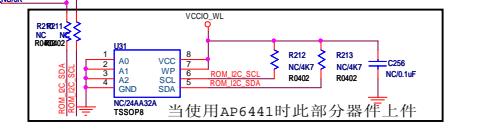
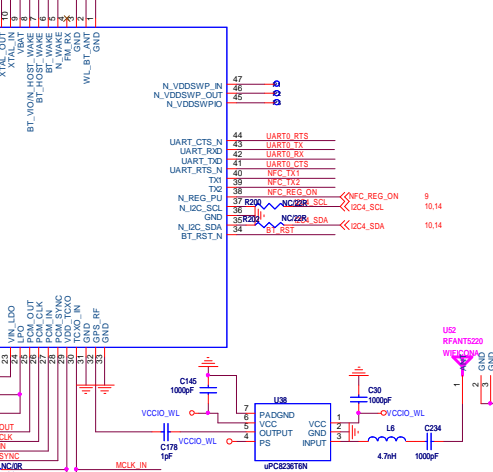
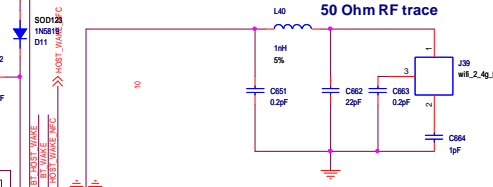


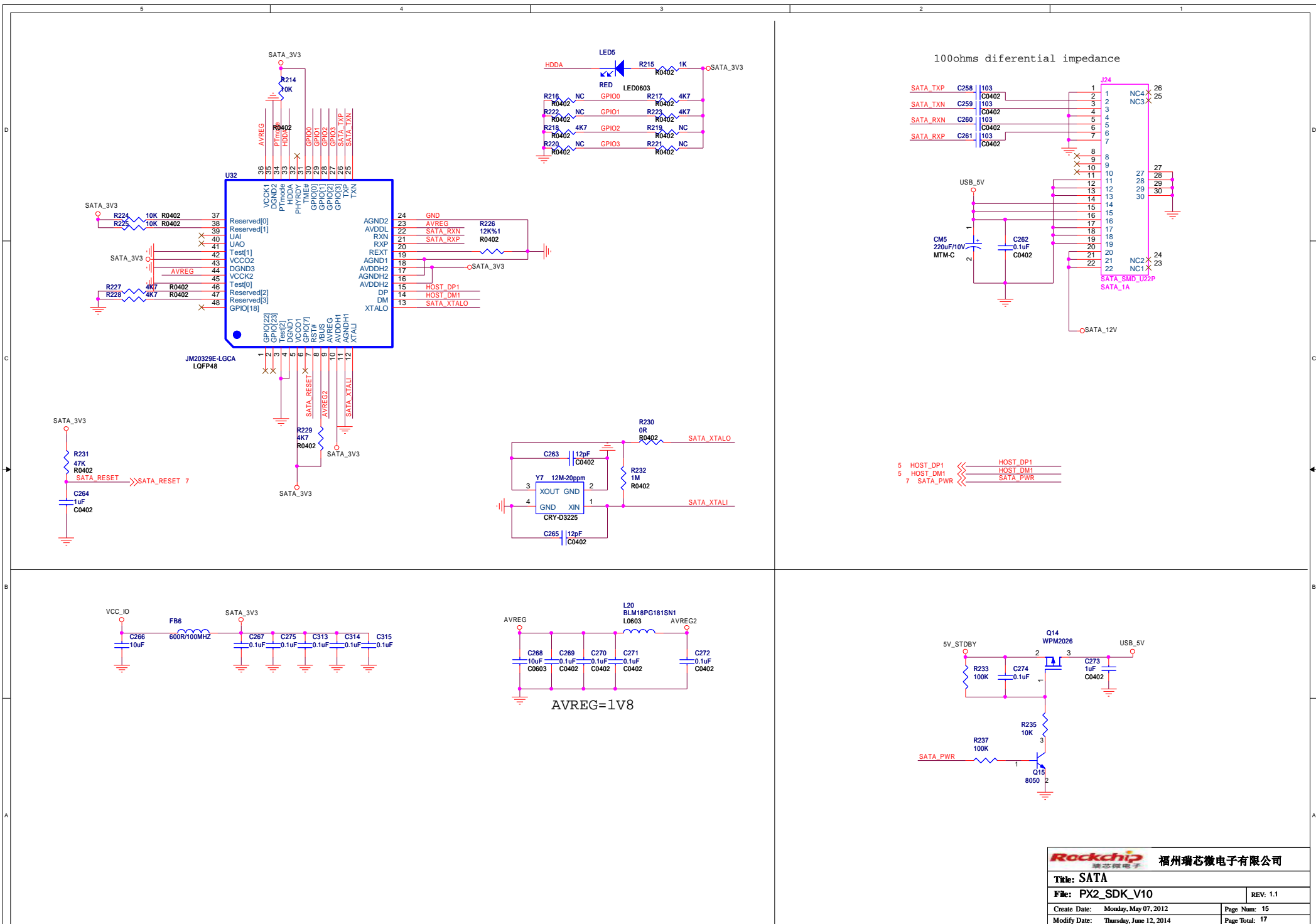
IR RECEIVE

当使用AP6493/AP6441时,R466需上件,R467/C688不上件

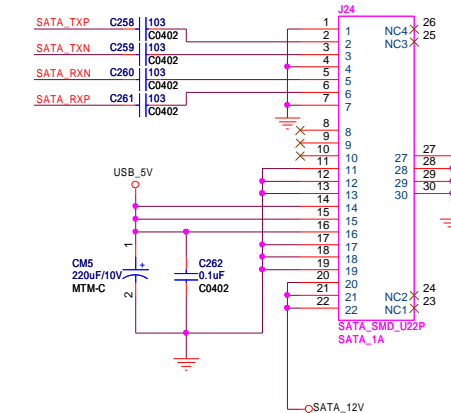
当使用AP6476时,R467/C688需上件, R466不上件

当使用AP6181/AP6210/AP6234/AP6330/AP6335时, R466/R467/C688不上件

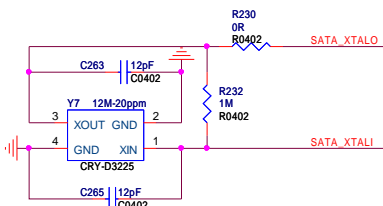
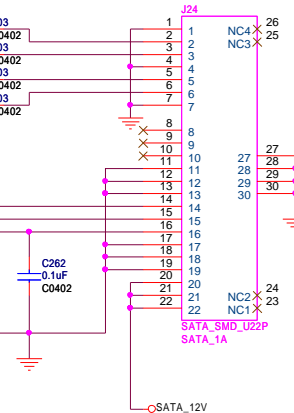




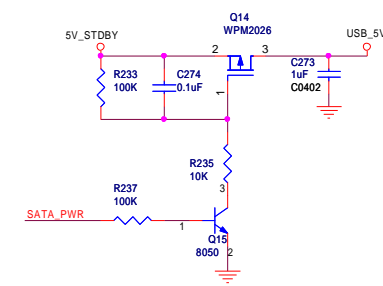
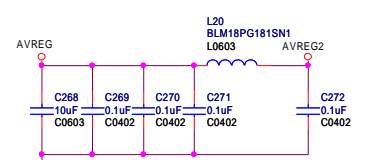
100ohms diferential impedance



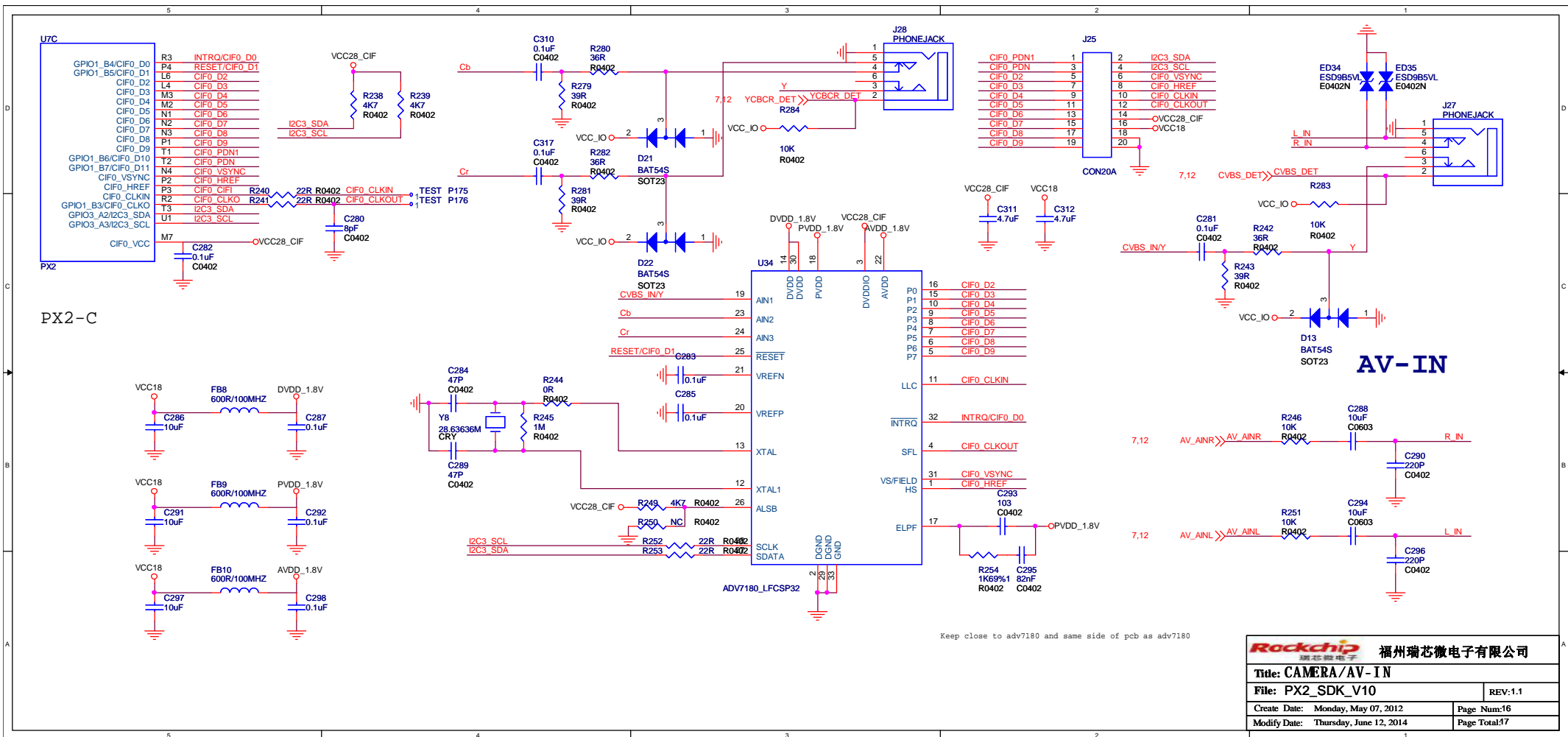
5 HOST_DP1
5 HOST_DM1
7 SATA_PWR

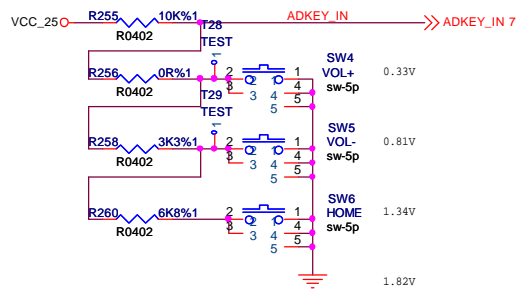


AVREG=1V8



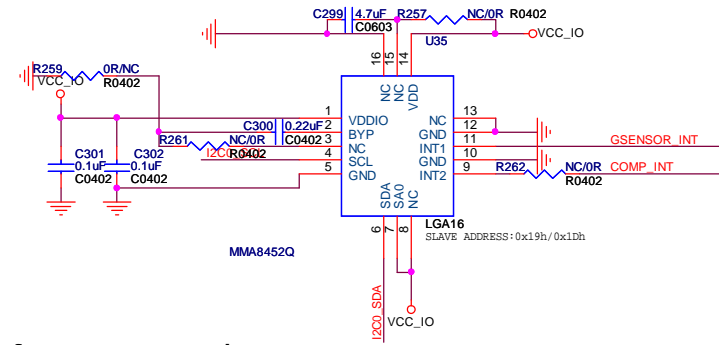
Rackchip 福州瑞芯微电子有限公司	
Title: SATA	
File: PX2_SDK_V10	REV: 1.1
Create Date: Monday, May 07, 2012	Page Num: 15
Modify Date: Thursday, June 12, 2014	Page Total: 17



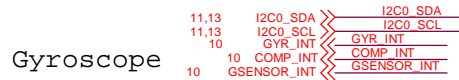
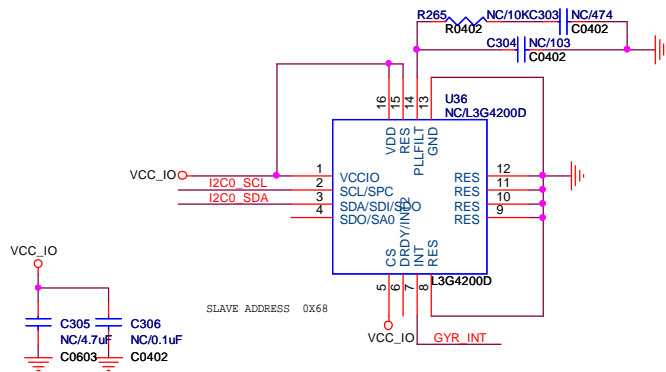


KEY BAORD

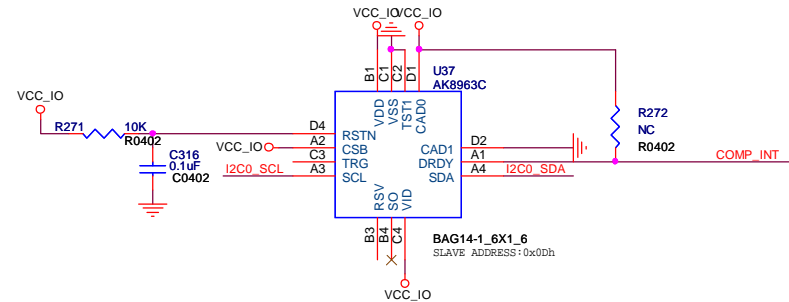
	LIS3DH/ MMA8452Q	LSM303D	KXT1K-1013
C303	NC	4.7uF	NC
R190	0ohm	NC	NC
R192	0ohm	NC	NC
C265	0.1uF	2.2uF	NC
U34	AK8963C	NC	NC
		R196 0R	
		R194 0R	



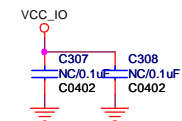
Note:
The first pin of G-sensor must be place on the lower left corner of PCB.
3D G-Sensor



Gyroscope



Compass



Title	<Title>	
Size	Document Number	Rev
B	<Doc>	<Rev Code>
Date:	Thursday, June 12, 2014	Sheet 17 of 17