

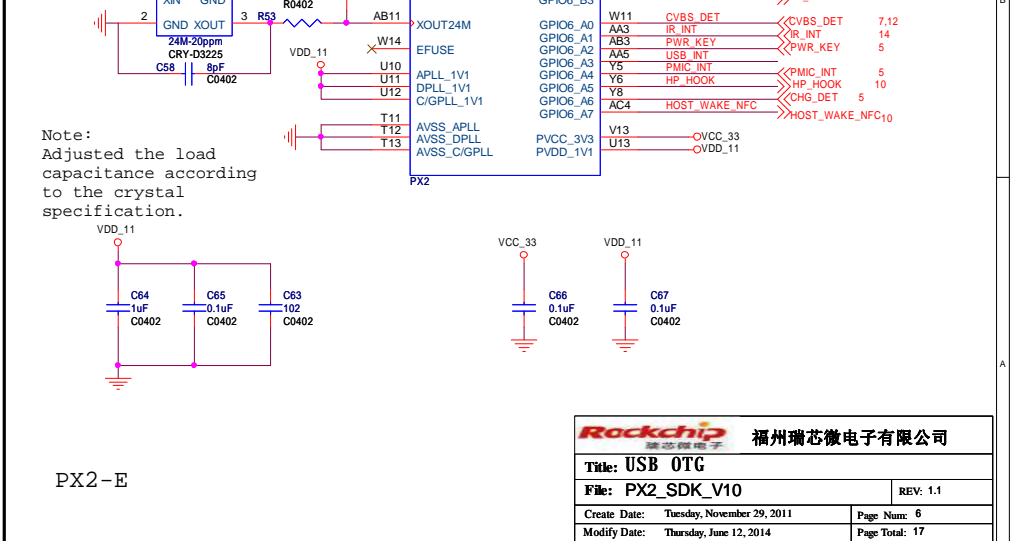
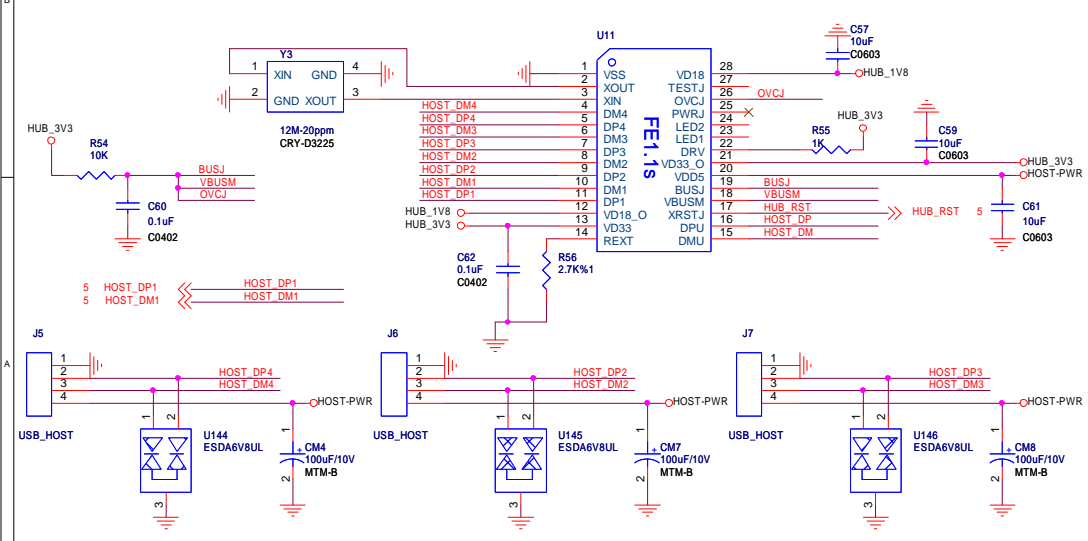
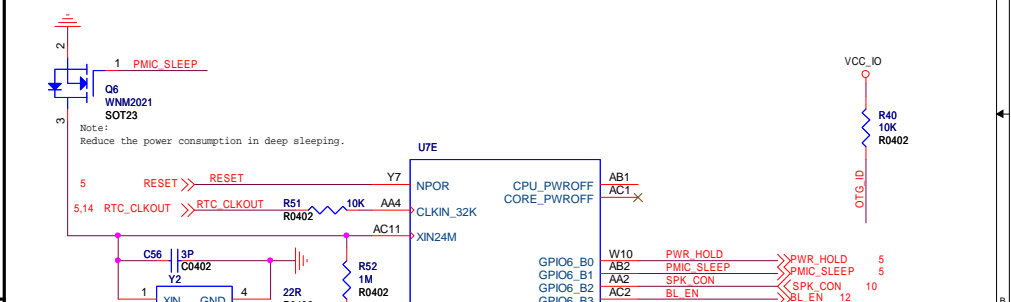
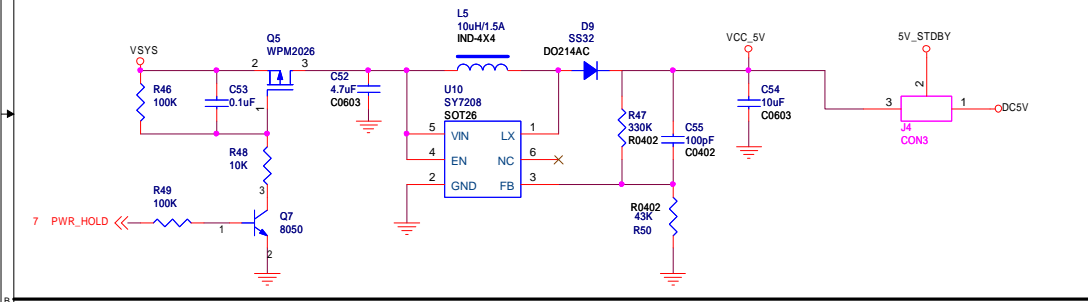
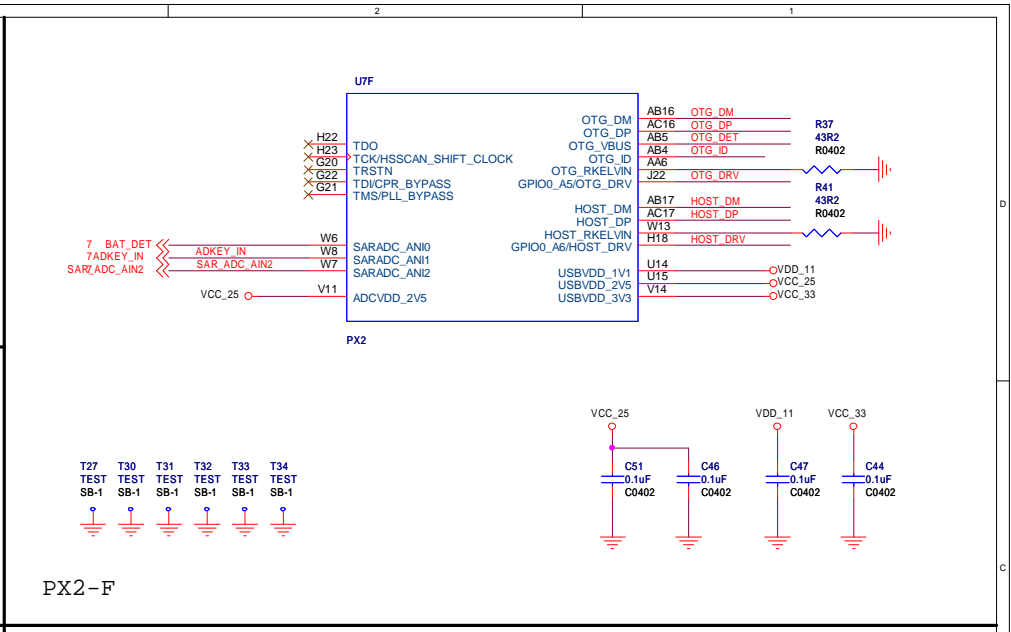
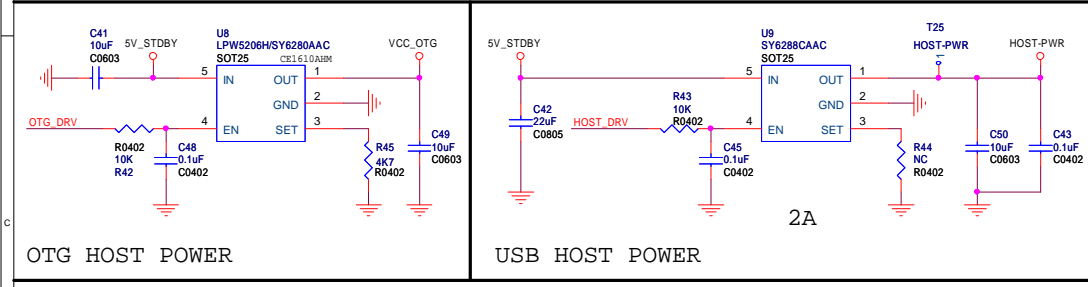
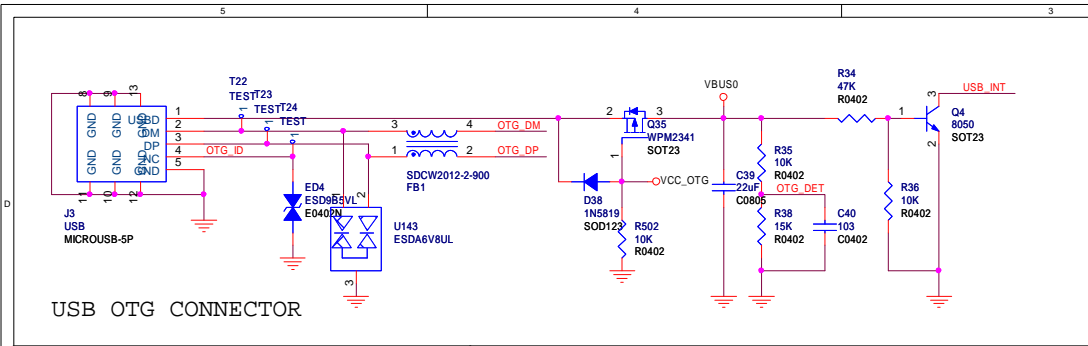
**Rackchip** 福州瑞芯微电子有限公司

**Title: System power**

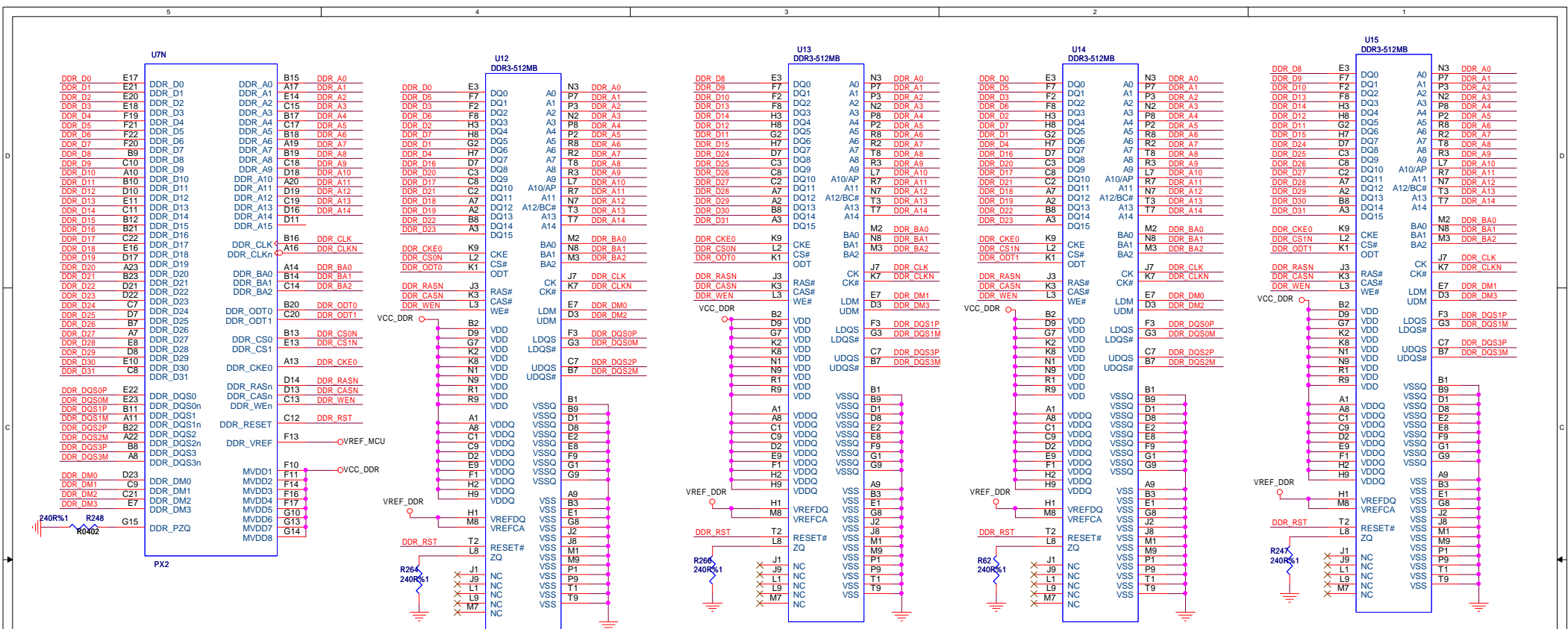
**File: PX2\_SDK\_V10** REV: 1.1

**Create Date: Tuesday, November 29, 2011** Page Num: 5

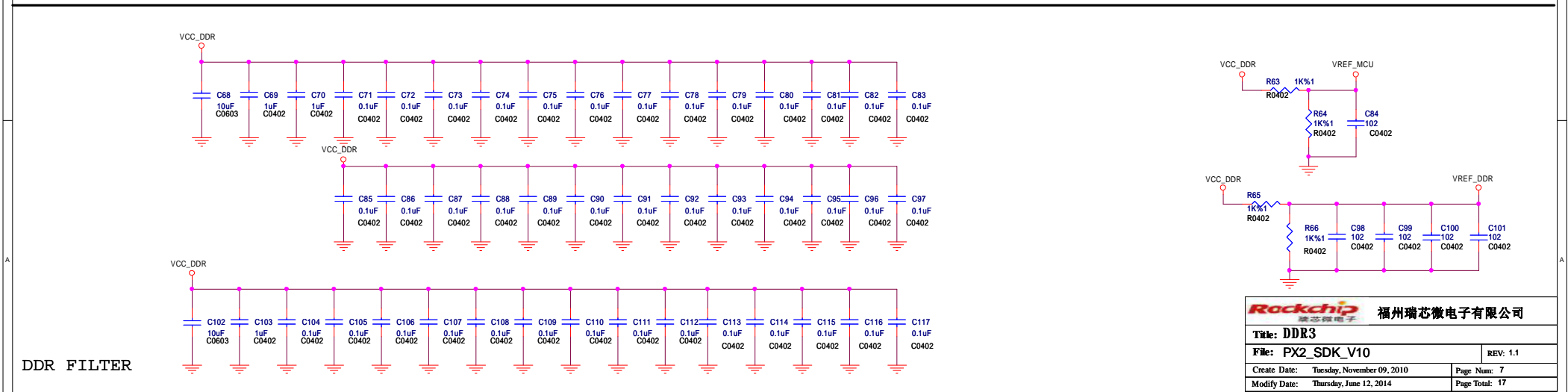
**Modify Date: Thursday, June 12, 2014** Page Total: 17

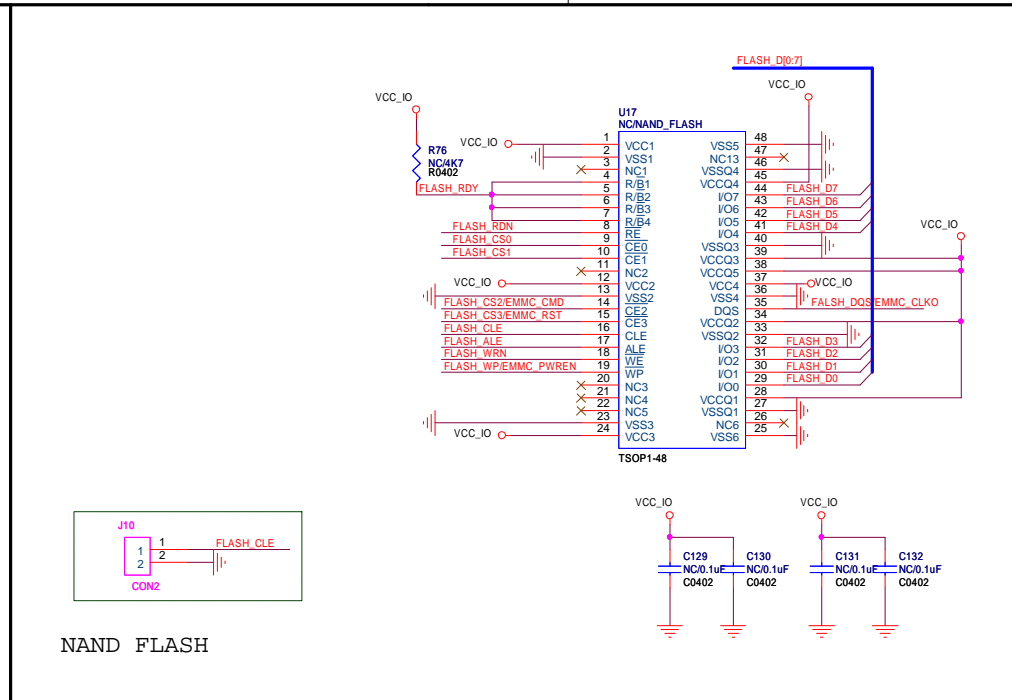
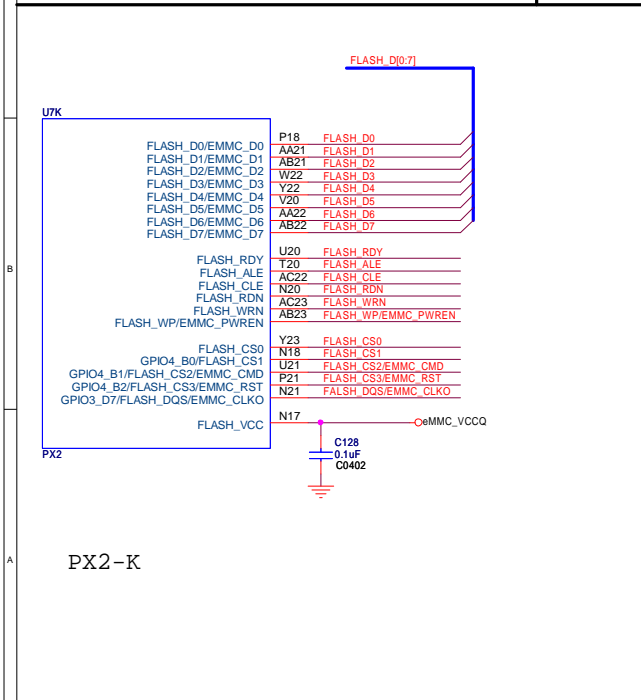
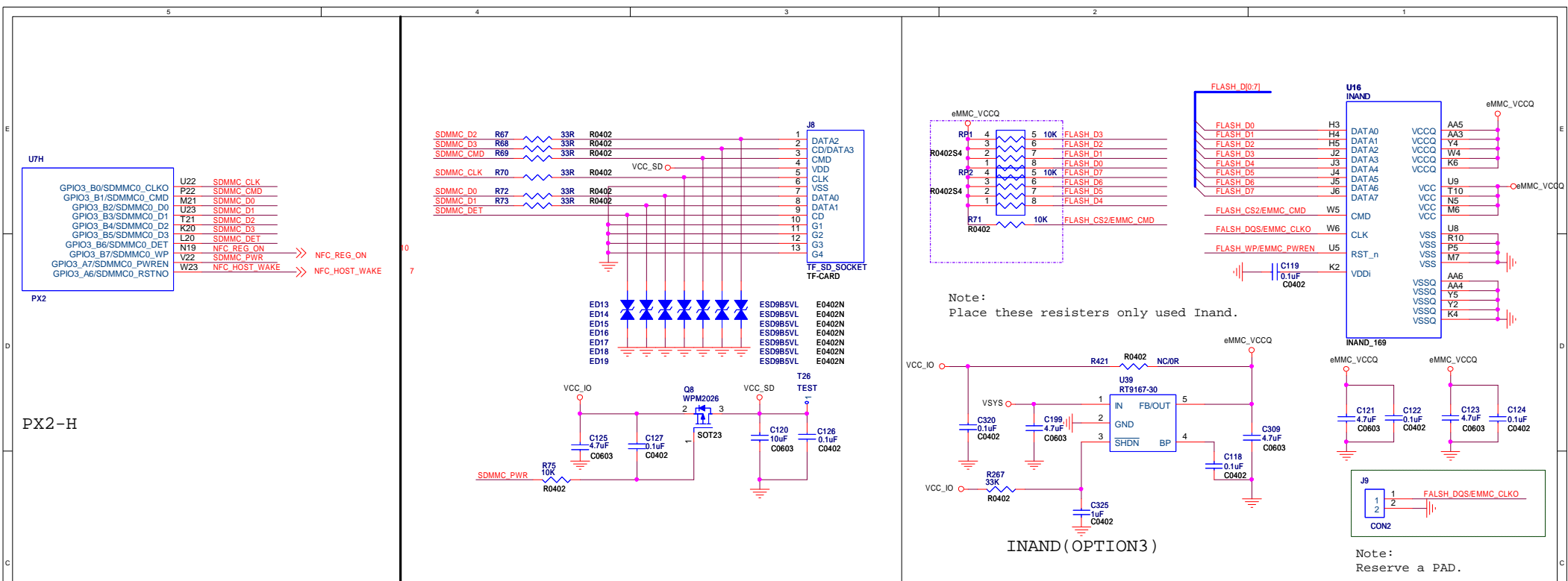


<b>Rackchip</b> 福州瑞芯微电子有限公司	
<b>Title: USB_OTG</b>	
<b>File: PX2_SDK_V10</b>	
Create Date: Tuesday, November 29, 2011	Page Name: 6
Modify Date: Thursday, June 12, 2014	Page Total: 17



PX2-N





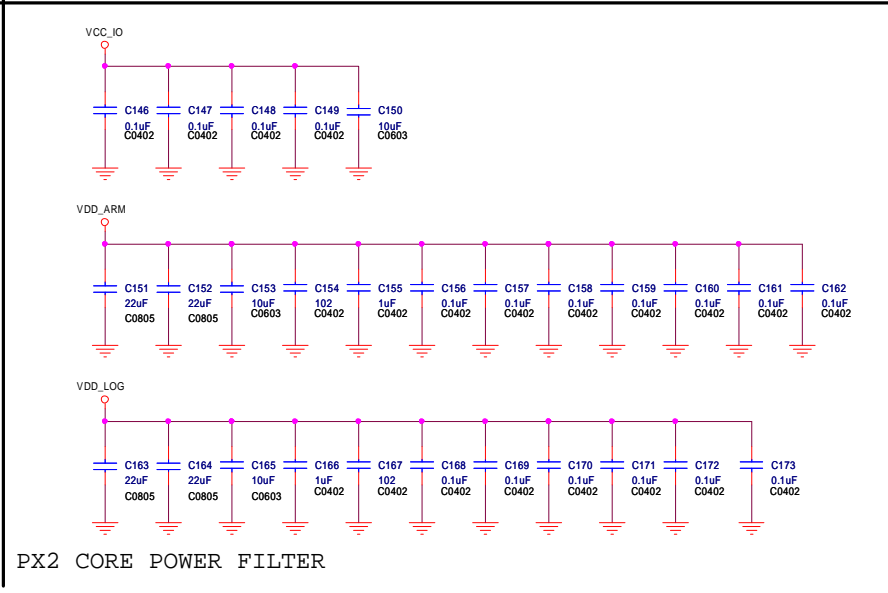
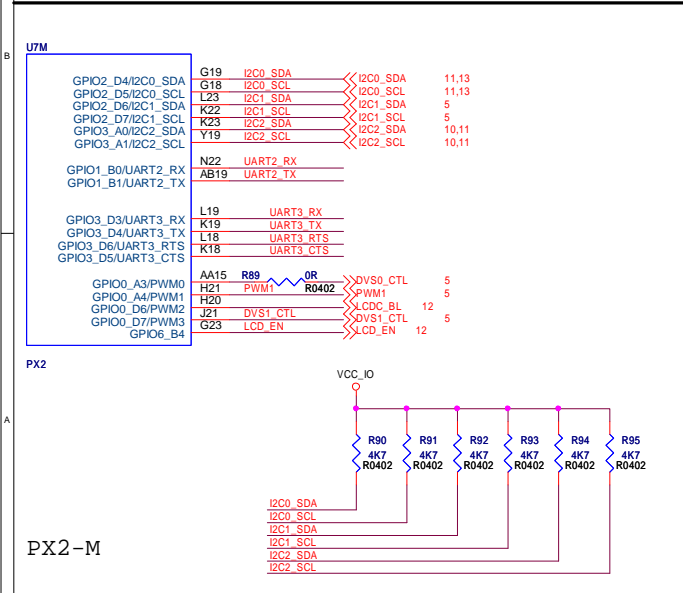
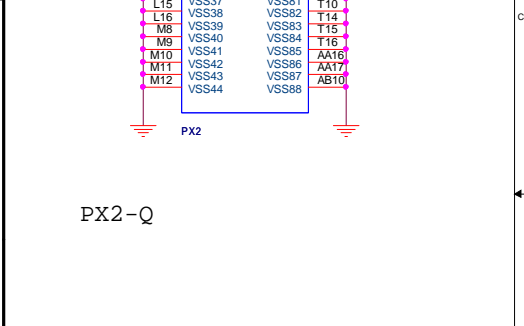
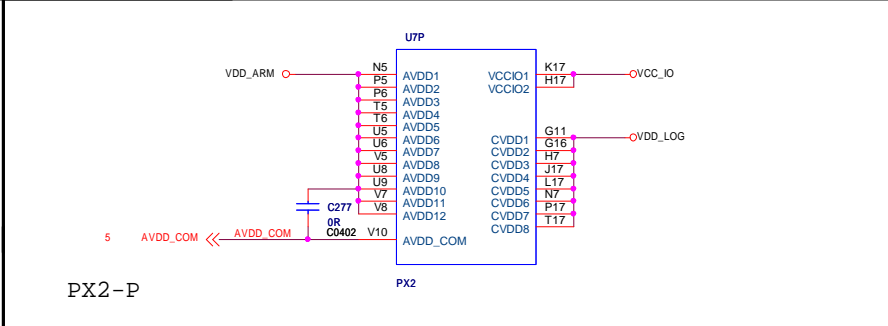
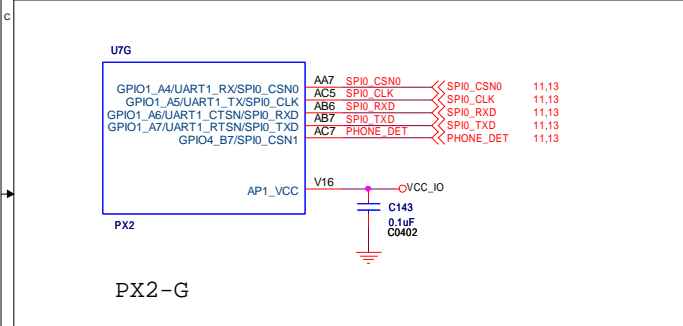
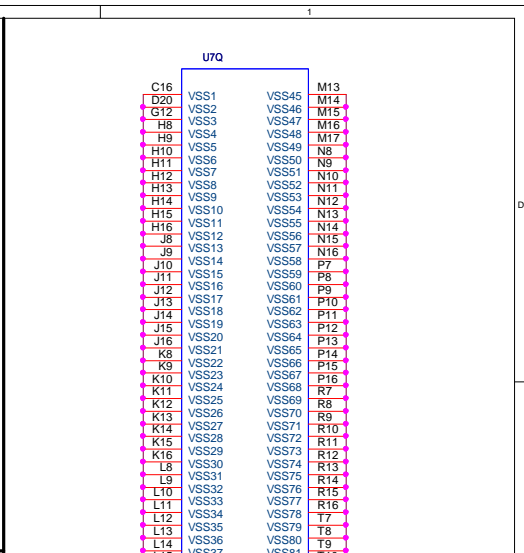
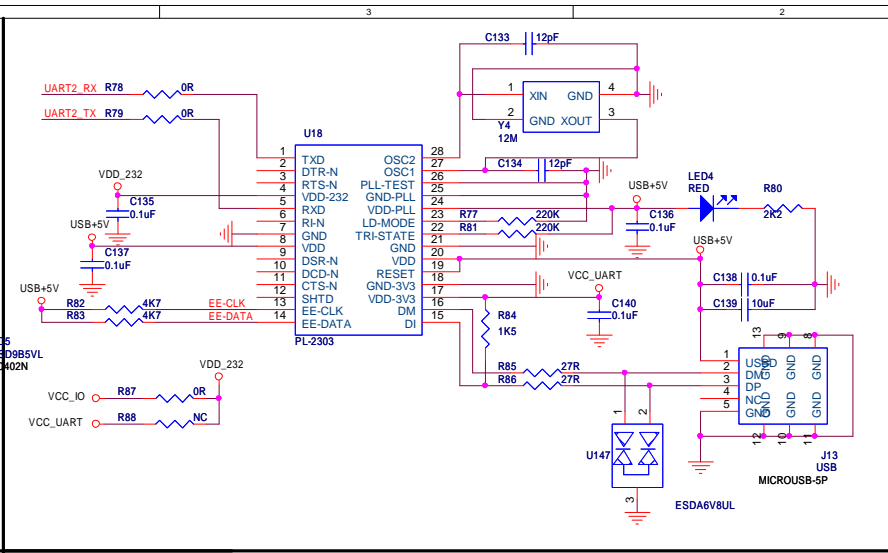
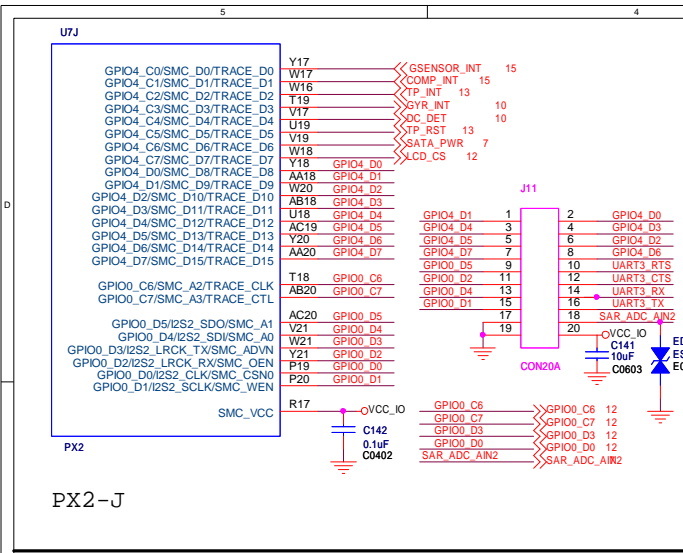
**Rockchip**  
福州瑞芯微电子有限公司

**Title: Nand Flash/SD**

**File: PX2\_SDK\_V10** REV: 1.1

Create Date: Thursday, October 14, 2010	Page Name: 8
Modify Date: Thursday, June 12, 2014	Page Total: 17



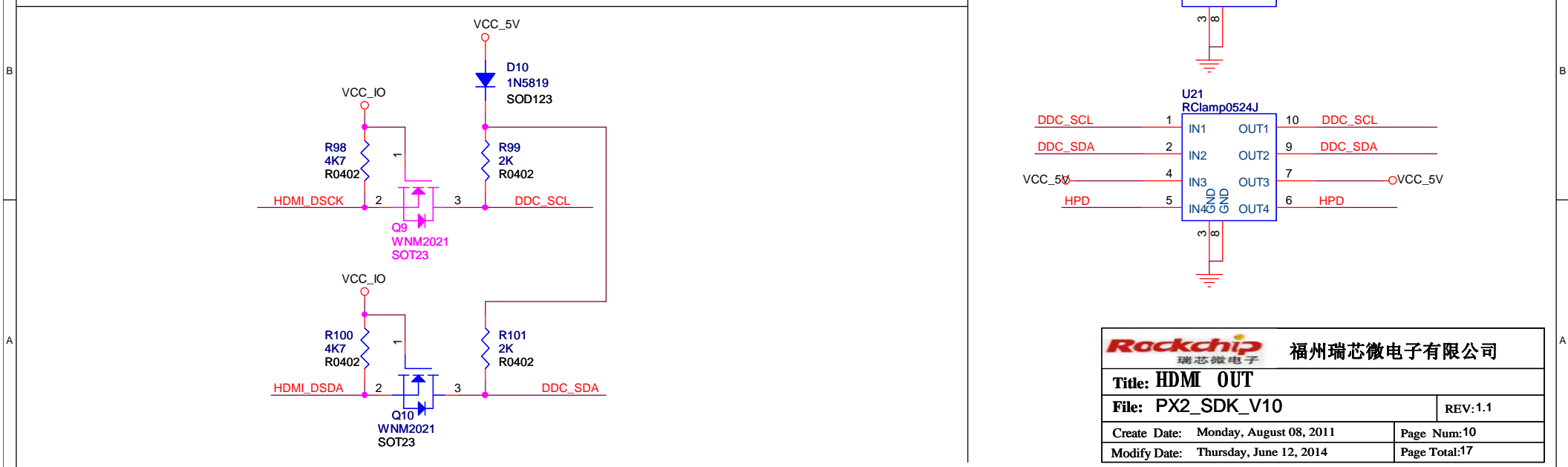
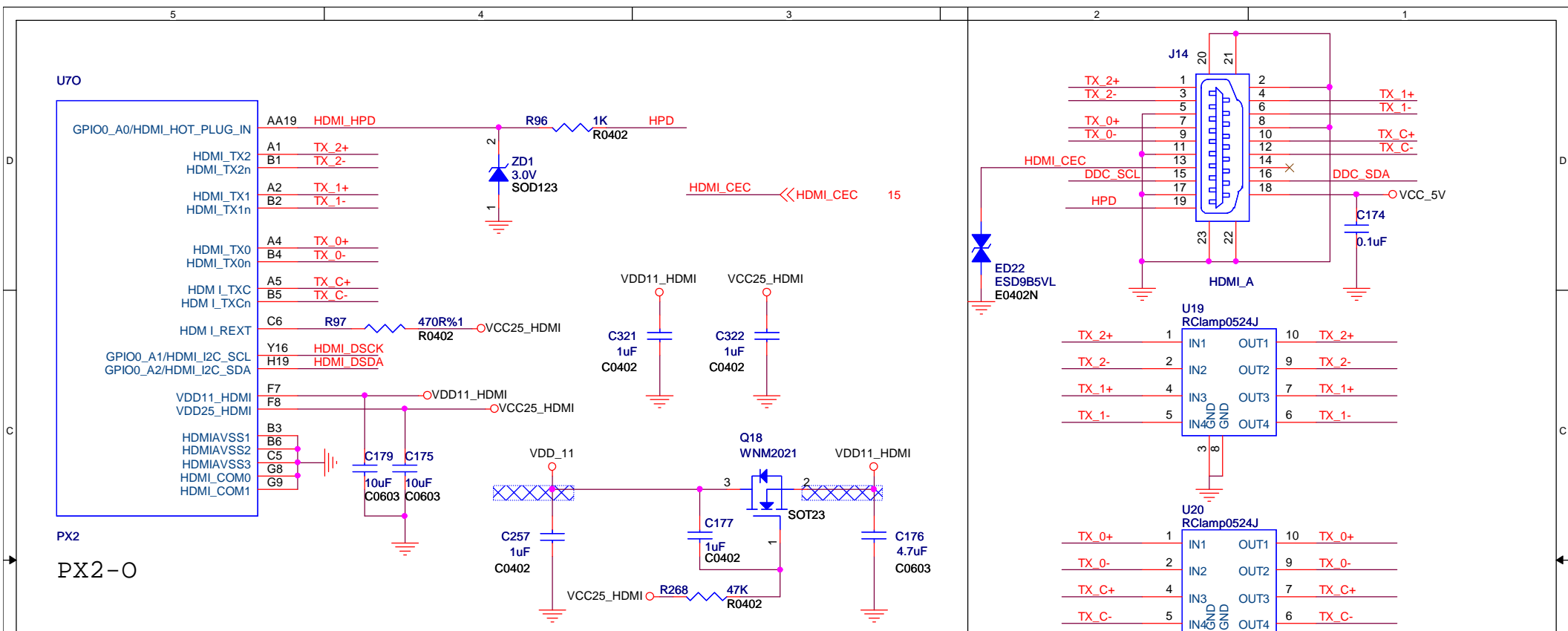


**Rockchip**  
 福州瑞芯微电子有限公司

**Title: GPIO/RK3066 POWER**

**File: PX2\_SDK\_V10** REV: 1.1

Create Date: Monday, October 18, 2010 Page Num: 9  
 Modify Date: Thursday, June 12, 2014 Page Total: 17



**Rockchip** 瑞芯微电子 福州瑞芯微电子有限公司

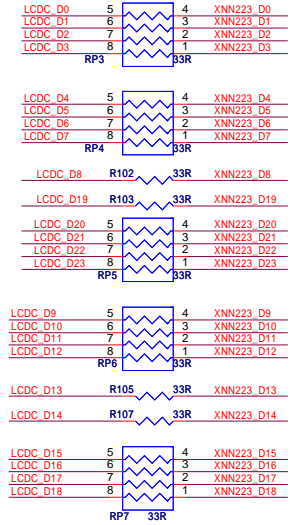
**Title: HDMI OUT**

**File: PX2\_SDK\_V10** REV:1.1

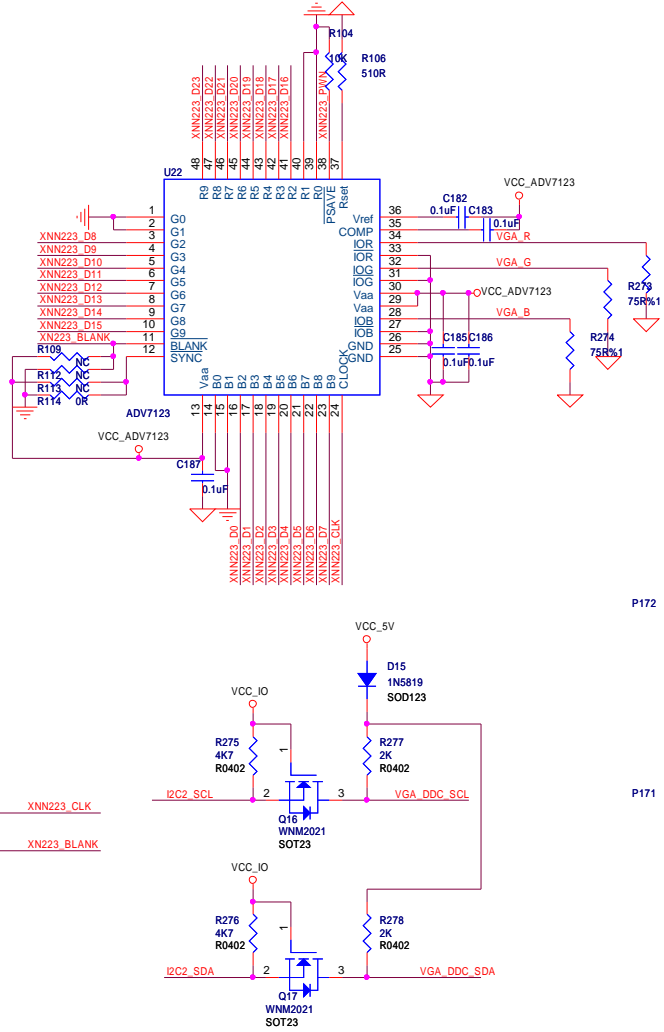
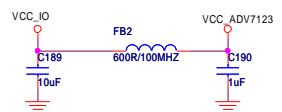
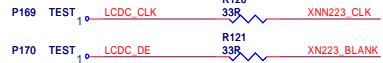
Create Date: Monday, August 08, 2011 Page Num:10

Modify Date: Thursday, June 12, 2014 Page Total:17

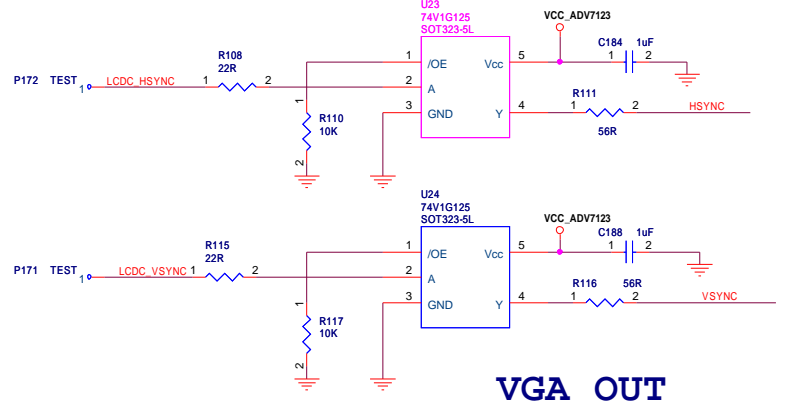
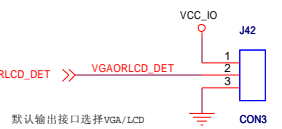
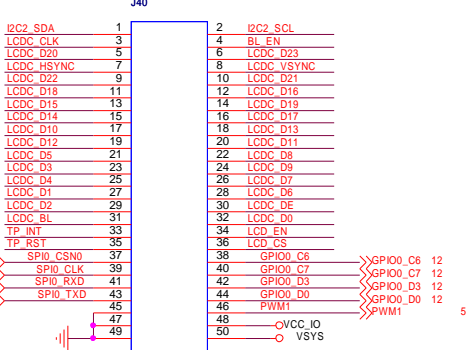
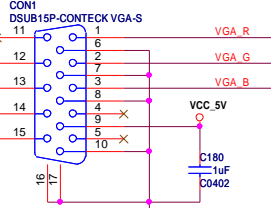
- U7A
- LCDO\_DEN
  - LCDO\_DCLK
  - LCDO\_VSYNC
  - LCDO\_HSYNC
  - LCDO\_D0
  - LCDO\_D1
  - LCDO\_D2
  - LCDO\_D3
  - LCDO\_D4
  - LCDO\_D5
  - LCDO\_D6
  - LCDO\_D7
  - LCDO\_D8
  - LCDO\_D9
  - LCDO\_D10
  - LCDO\_D11
  - LCDO\_D12
  - LCDO\_D13
  - LCDO\_D14
  - LCDO\_D15
  - LCDO\_D16
  - LCDO\_D17
  - LCDO\_D18
  - LCDO\_D19
  - LCDO\_D20
  - LCDO\_D21
  - LCDO\_D22
  - LCDO\_D23
  - LCDO\_VCC0
  - LCDO\_VCC1



PX2-A



VGA OUT

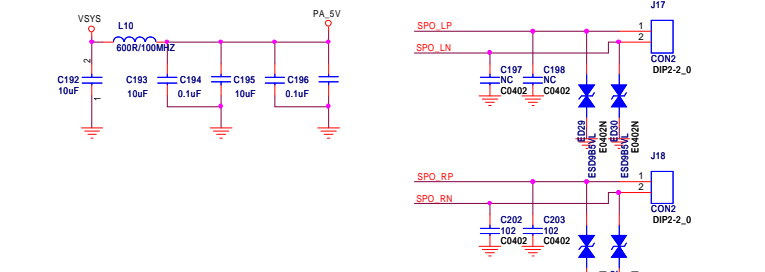
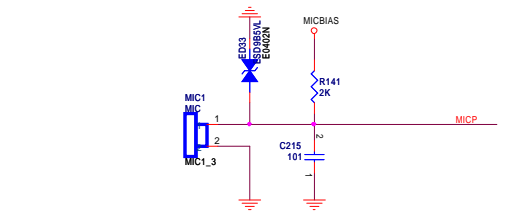
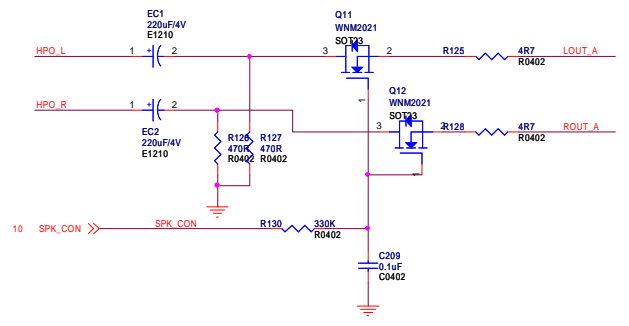


VGA OUT

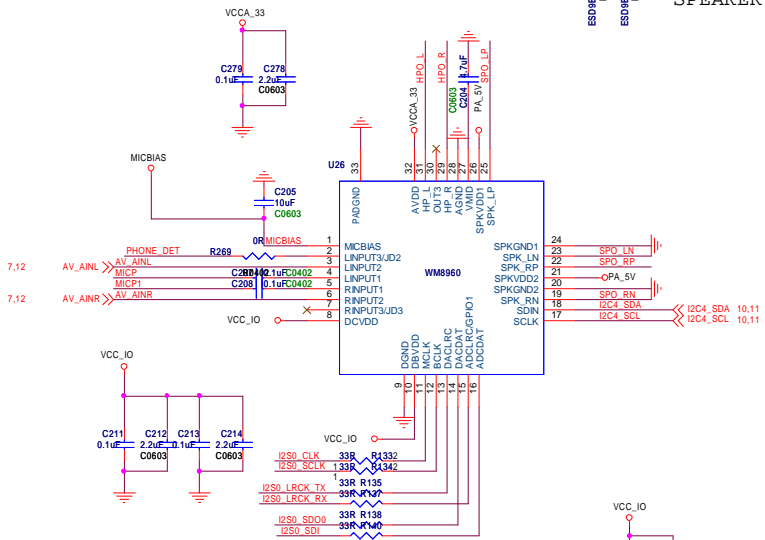
<b>Rackchip</b> 福州瑞芯微电子有限公司	
<b>Title: YPbPr/CVBS/VGA OUT</b>	
<b>File: PX2_SDK_V10</b>	<b>REV: 1.1</b>
<b>Create Date: Friday, October 15, 2010</b>	<b>Page Num: 11</b>
<b>Modify Date: Thursday, June 12, 2014</b>	<b>Page Total: 17</b>

U7L		
GPIO0_A7/I2S0_SDI	L21	I2S0_SDI
GPIO0_B0/I2S0_CLK	L22	I2S0_CLK
GPIO0_B1/I2S0_SCLK	R23	I2S0_SCLK
GPIO0_B2/I2S0_LRCK_RX	R22	I2S0_LRCK_RX
GPIO0_B3/I2S0_LRCK_TX	R21	I2S0_LRCK_TX
GPIO0_B4/I2S0_D0	K21	I2S0_SDO0
GPIO0_B5/I2S0_D1	D23	SATA_RESET 7
GPIO0_B6/I2S0_D2	N23	VGAORLCD_DET <<<VGAORLCD_DET 7,12
GPIO0_B7/I2S0_D3	M22	YCBCR_DET <<<YCBCR_DET 7,12
GPIO1_B2/SPDIF_TX	L22	SPDIF_TX

PX2-L

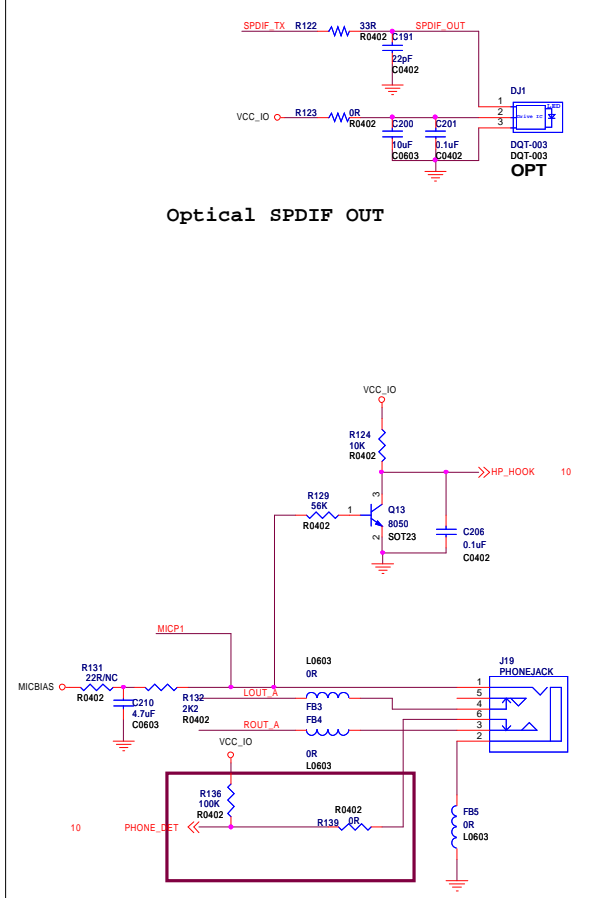


SPEAKER

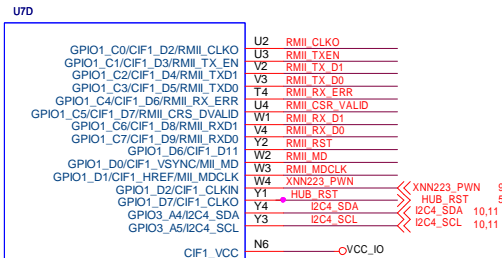


CODEC

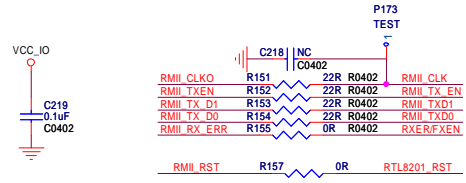
Optical SPDIF OUT



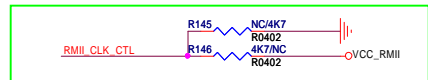
<b>Rockchip</b> 福州瑞芯微电子有限公司	
<b>Title: SPDIF OUT/AUDIO</b>	
<b>File: PX2_SDK_V10</b>	
Create Date: Monday, May 07, 2012	Page Num: 12
Modify Date: Thursday, June 12, 2014	Page Total: 17



PX2-D

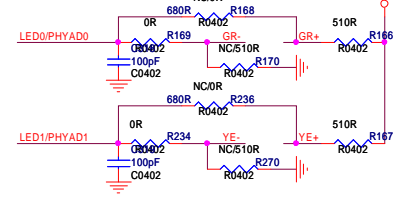


### RMII REF\_CLK direction



Pull Low for RMII REF\_CLK Output mode  
Pull High for RMII REF\_CLK Input mode(default)

### PHY Address/LED



### MII/RMII Selection



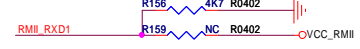
Pull High for RMII mode(default)

### UTP / Fiber Selection

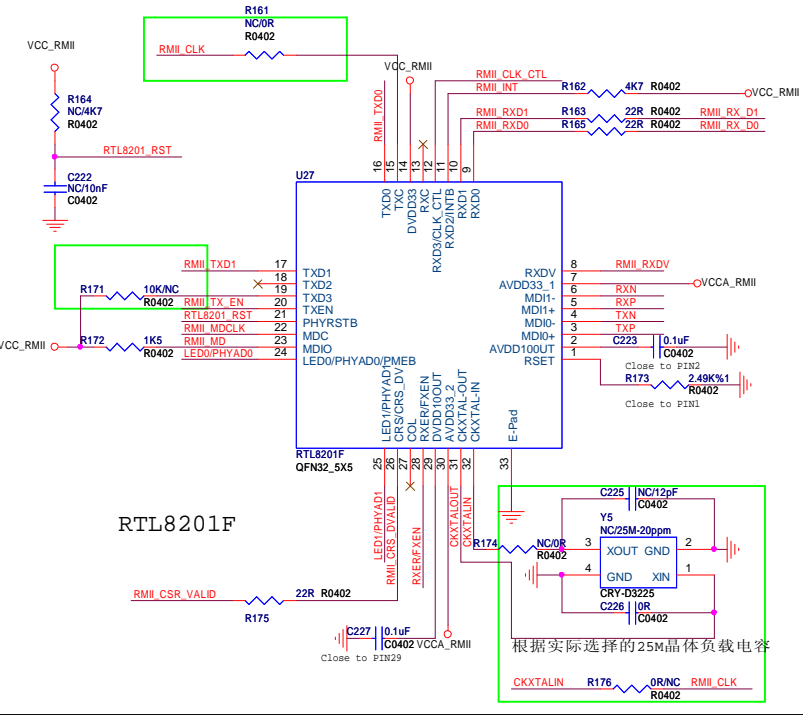


Pull Low for UTP Mode(default)  
Pull High for Fiber Mode

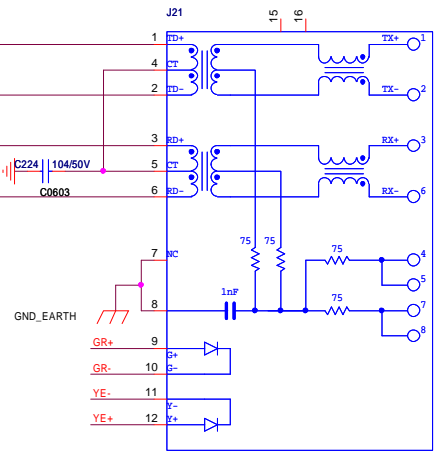
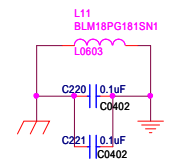
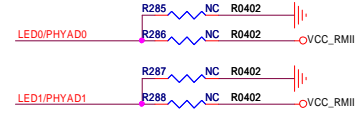
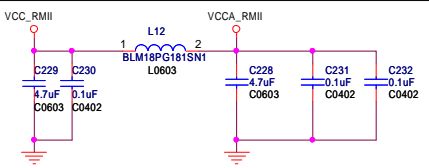
### WOL/LED0 Selection



Pull Low for LED0 Mode(default)  
Pull High for WOL Mode  
WOL:Wake-on-LAN

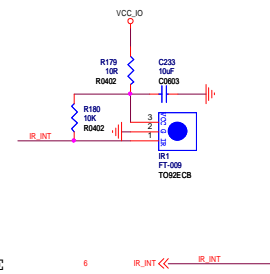
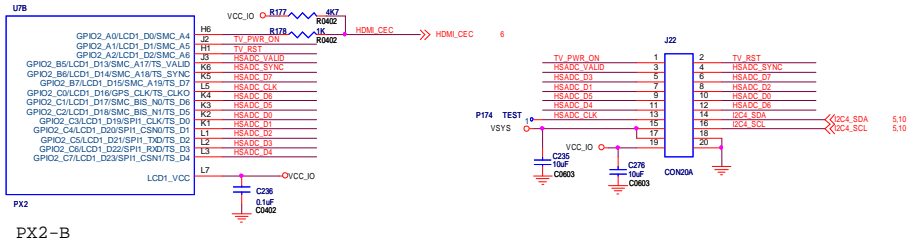


RTL8201F POWER

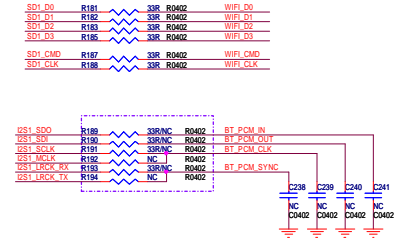


HR911105A

<b>Rackchip</b> 福州瑞芯微电子股份有限公司	
<b>Title: RMII Ethernet</b>	
<b>File: PX2_SDK_V10</b>	REV: 1.1
Create Date: Friday, October 15, 2010	Page Num: 13
Modify Date: Thursday, June 12, 2014	Page Total: 17

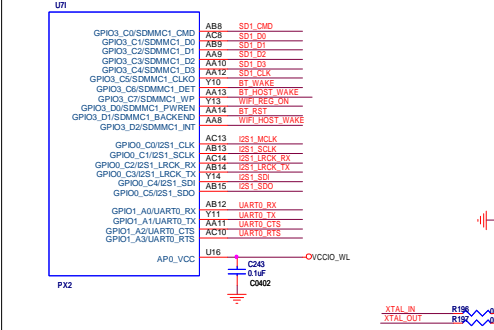
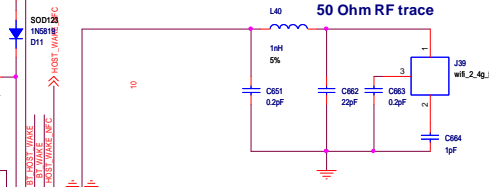


当使用AP6493/AP6441时,R466需上件,R467/C688不上件

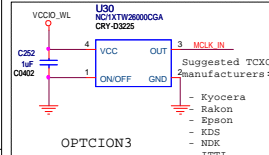


当使用AP6476时,R467/C688需上件, R466不上件

当使用AP6181/AP6210/AP6234/AP6330/AP6335时, R466/R467/C688不上件



Note :PLS design the RF under RK RF LAYOUT guide;  
For more suggestions, please refer to the SPEC of the wireless IC



当使用 AP6181/AP6330/AP6441/AP6234时, R449/R461电阻皆需移除不上件,  
当使用 AP6210/AP6476/AP6493时, R449电阻要上件,R461电阻要移除不上件,  
当使用 AP6335时, R449电阻需移除不上件, R461电阻要上件选择SDIO2.0/3.0 (VIO1.8V状态)

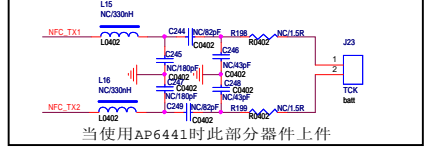
当使用AP6441时此部分器件上件

当使用AP6210/AP6493时,OPTCION1/2皆需要上件,OPTCION3 移除

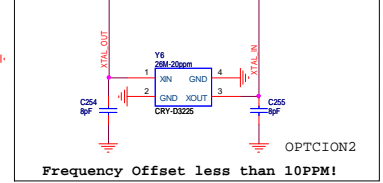
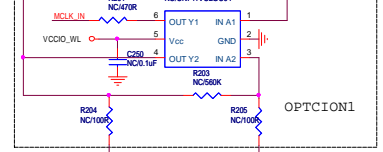
当使用AP6181/AP6330/AP6335/AP6441/AP6234时, OPTCION2需要上件,OPTCION1/3 移除

当使用AP6335时,晶振器件Y6要更改为37.4MHz  
当使用AP6476时,框A,OPTCION1/2皆不上件,OPTCION3上件.

NFC\_TX1/TX2的走线不能小于12mil;L20/L21不用平行布局,垂直布局,防止互感

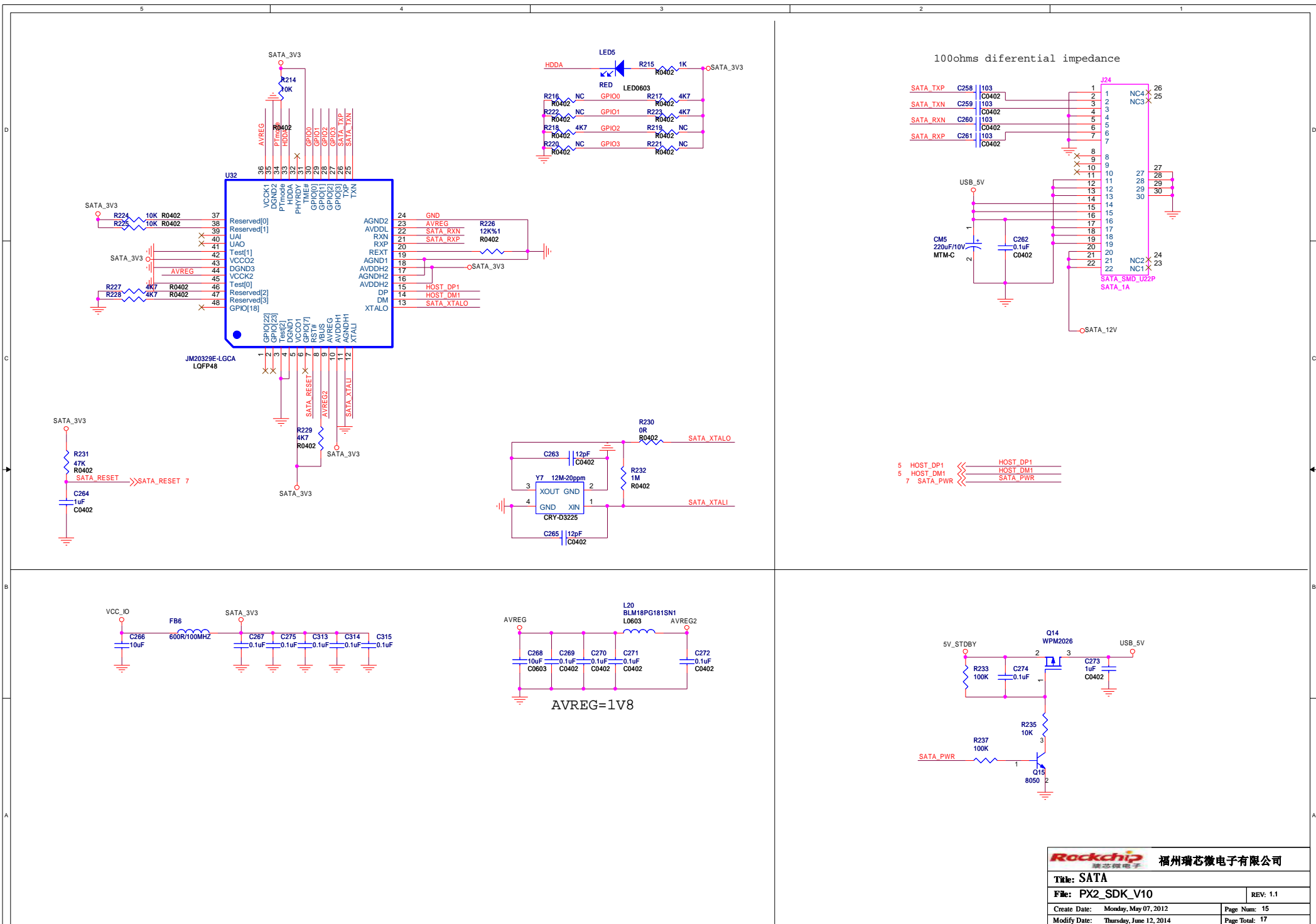


当使用AP6441时此部分器件上件

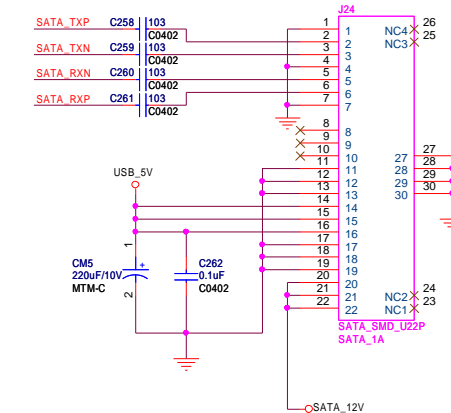


Frequency Offset less than 10PPM!

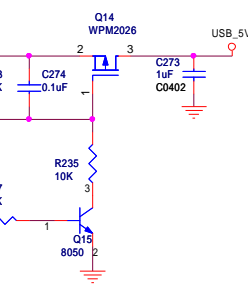
		福州瑞芯微电子有限公司
Title: WIFI+BT/IR		
File: PX2_SDK_V10		
Create Date: Friday, October 15, 2010	Page Num: 14	
Modify Date: Thursday, June 12, 2014	Page Total: 17	



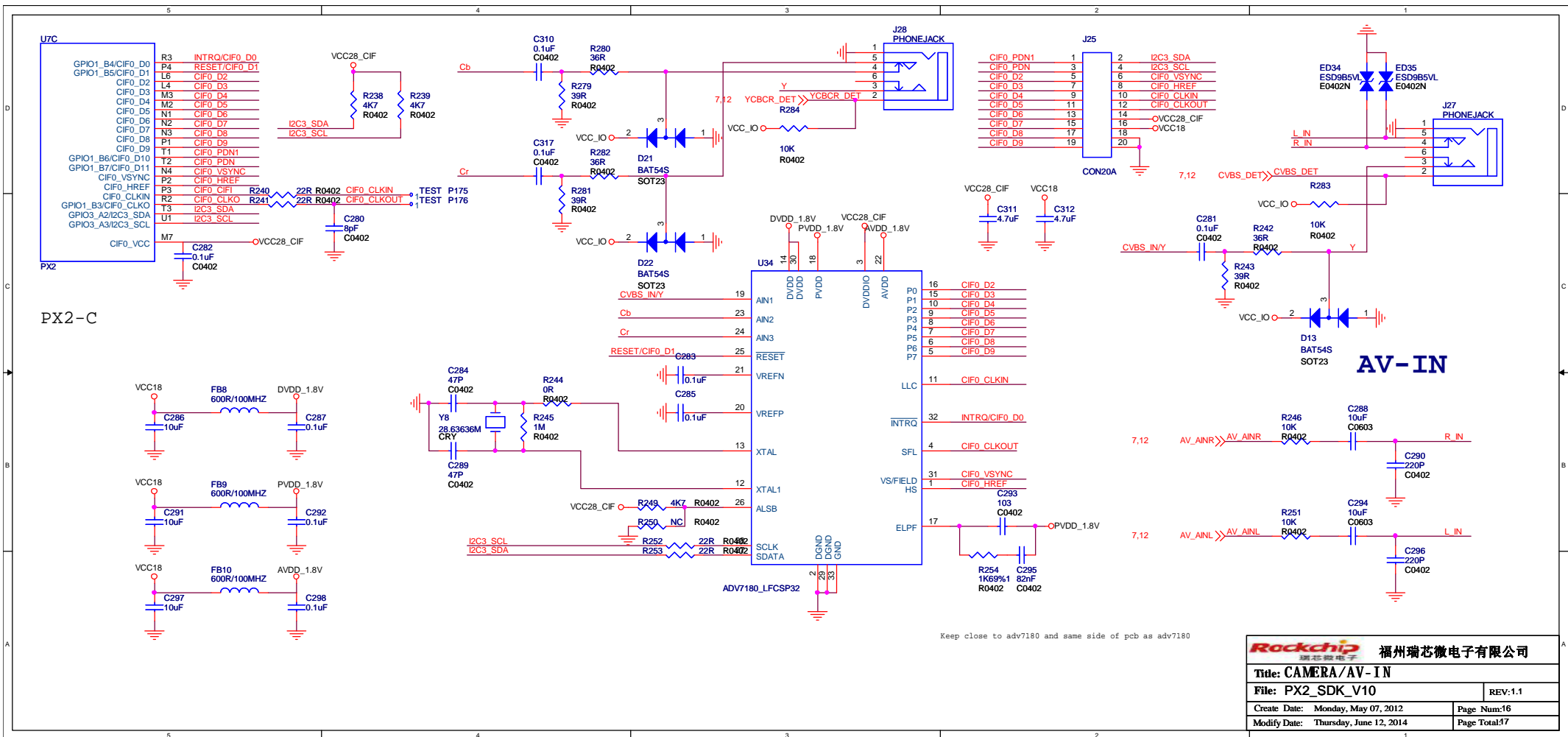
100ohms diferential impedance



5 HOST\_DP1  
5 HOST\_DM1  
7 SATA\_PWR

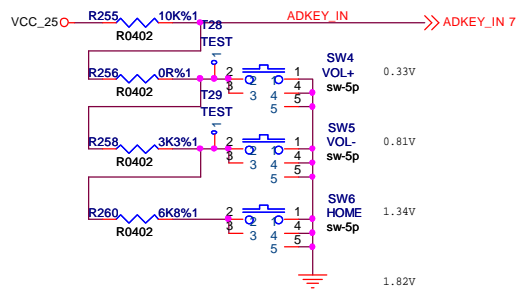


<b>Rackchip</b> 福州瑞芯微电子有限公司	
Title: SATA	
File: PX2_SDK_V10	REV: 1.1
Create Date: Monday, May 07, 2012	Page Num: 15
Modify Date: Thursday, June 12, 2014	Page Total: 17



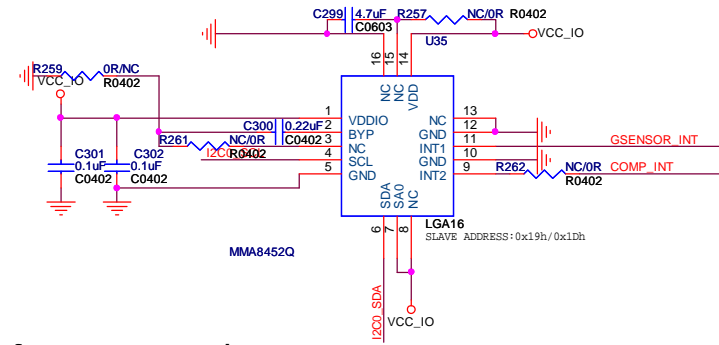
<b>Rockchip</b> 瑞芯微电子		<b>福州瑞芯微电子有限公司</b>	
<b>Title: CAMERA/AV-IN</b>			
<b>File: PX2_SDK_V10</b>			
Create Date: Monday, May 07, 2012	REV:1.1		
Modify Date: Thursday, June 12, 2014	Page Num:16		
	Page Total:17		



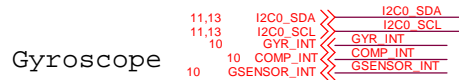
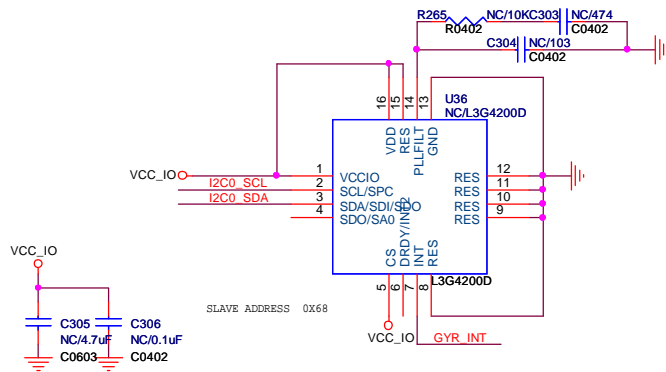


KEY BAORD

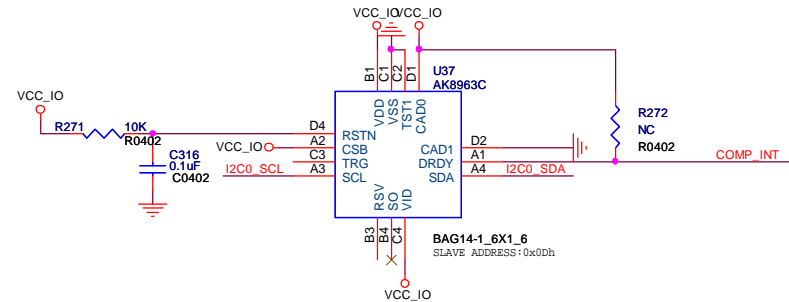
	LIS3DH/ MMA8452Q	LSM303D	KXT1K-1013
C303	NC	4.7uF	NC
R190	0ohm	NC	NC
R192	0ohm	NC	NC
C265	0.1uF	2.2uF	NC
U34	AK8963C	NC	NC
		R196 0R	
		R194 0R	



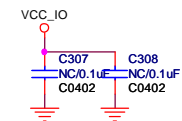
Note:  
The first pin of G-sensor must be place on the lower left corner of PCB.  
3D G-Sensor



Gyroscope



Compass



Title	<Title>	
Size	Document Number	Rev
B	<Doc>	<Rev Code>
Date:	Thursday, June 12, 2014	Sheet 17 of 17