

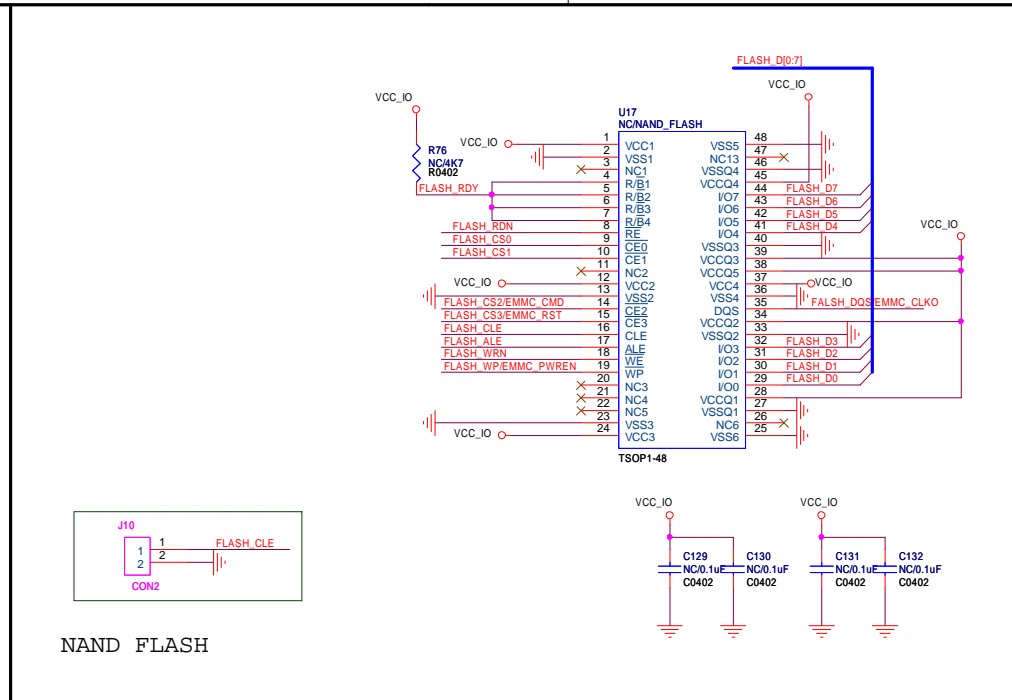
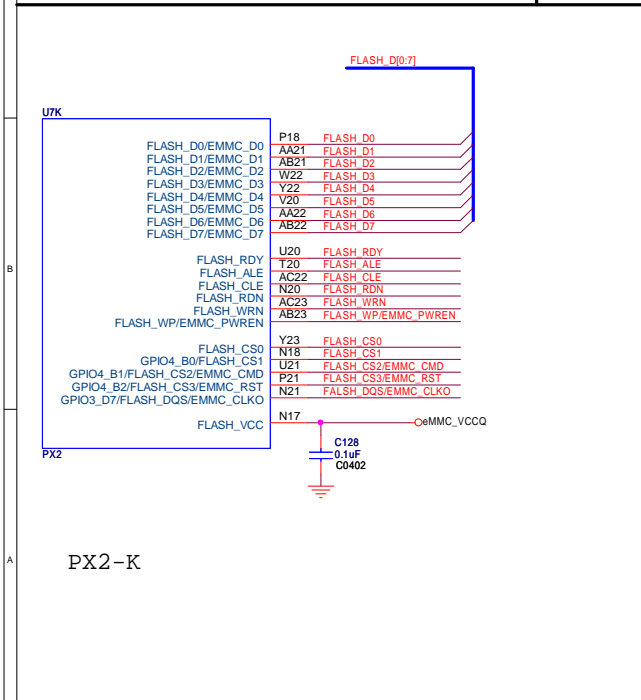
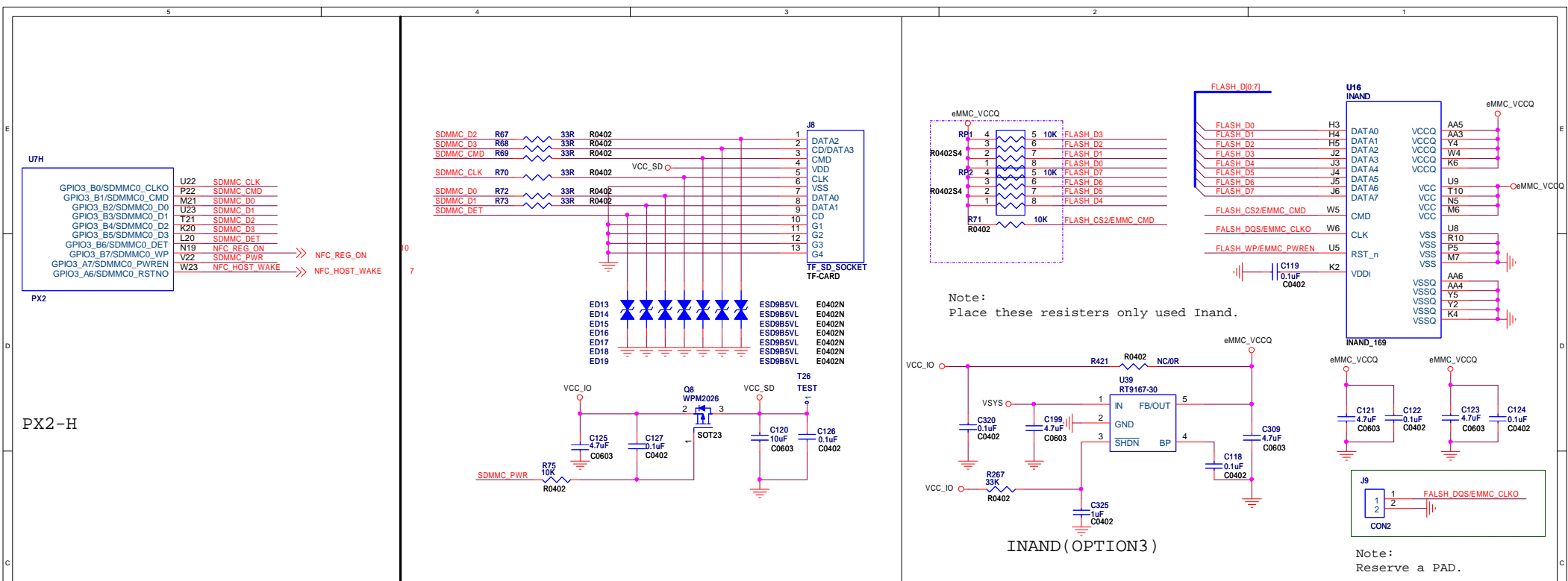
Rackchip 福州瑞芯微电子有限公司

Title: System power

File: PX2_SDK_V10 REV: 1.1

Create Date: Tuesday, November 29, 2011 Page Num: 5

Modify Date: Thursday, June 12, 2014 Page Total: 17

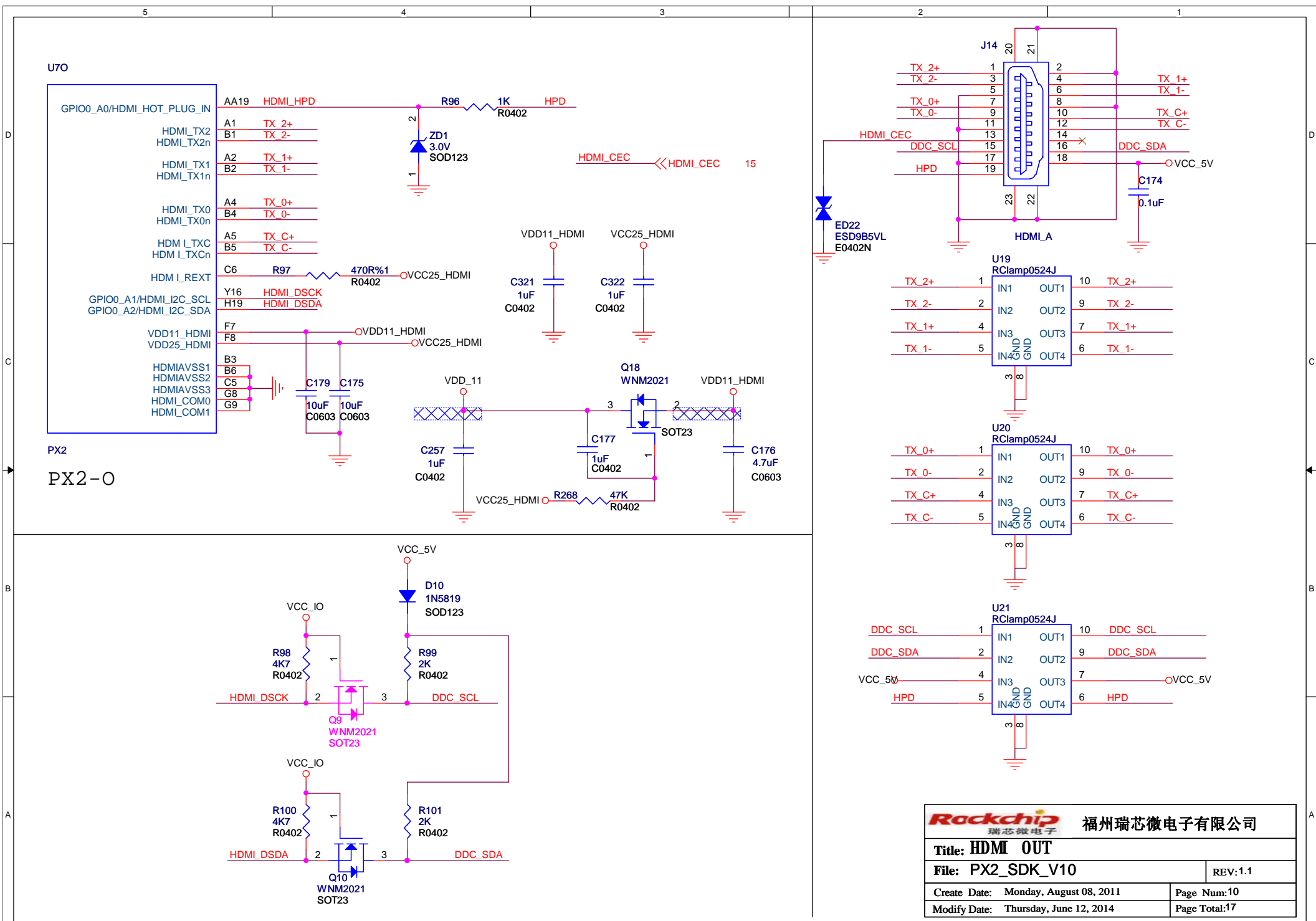


Rockchip
福州瑞芯微电子有限公司

Title: Nand Flash/SD

File: PX2_SDK_V10 REV: 1.1

Create Date: Thursday, October 14, 2010	Page Name: 8
Modify Date: Thursday, June 12, 2014	Page Total: 17

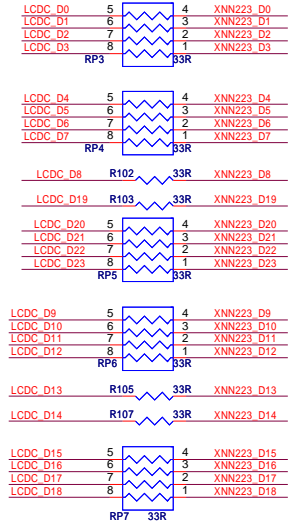


Rockchip
瑞芯微电子

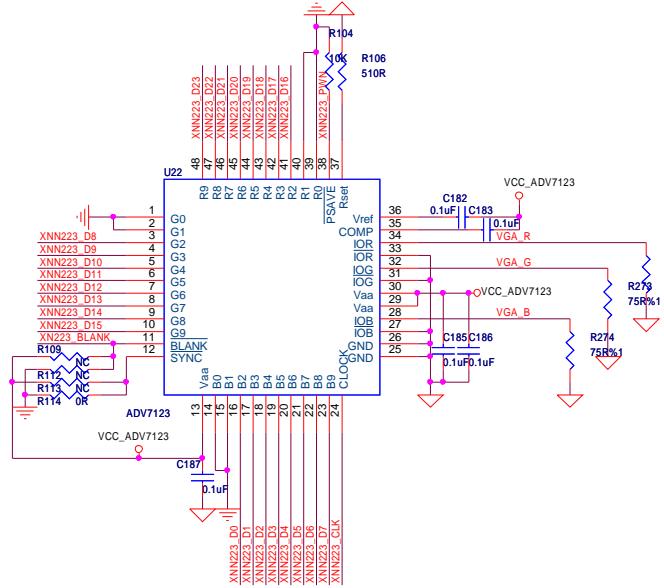
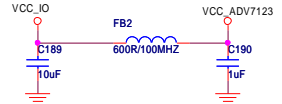
福州瑞芯微电子有限公司

Title: HDMI OUT	
File: PX2_SDK_V10	REV:1.1
Create Date: Monday, August 08, 2011	Page Num:10
Modify Date: Thursday, June 12, 2014	Page Total:17

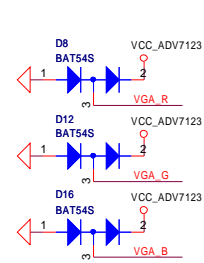
- U7A
- LCDO_DEN
 - LCDO_DCLK
 - LCDO_VSYNC
 - LCDO_HSYNC
 - LCDO_D0
 - LCDO_D1
 - LCDO_D2
 - LCDO_D3
 - LCDO_D4
 - LCDO_D5
 - LCDO_D6
 - LCDO_D7
 - LCDO_D8
 - LCDO_D9
 - LCDO_D10
 - LCDO_D11
 - LCDO_D12
 - LCDO_D13
 - LCDO_D14
 - LCDO_D15
 - LCDO_D16
 - LCDO_D17
 - LCDO_D18
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 - LCDO_D21
 - LCDO_D22
 - LCDO_D23
 - LCDO_VCC0
 - LCDO_VCC1



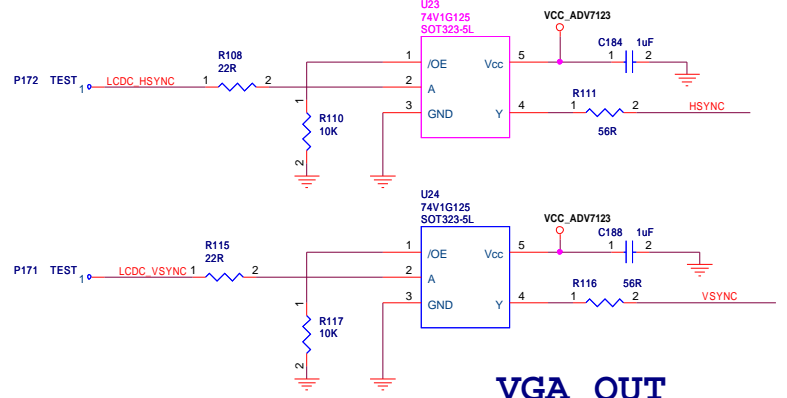
PX2-A



VGA OUT



默认输出口选择VGA/LCD

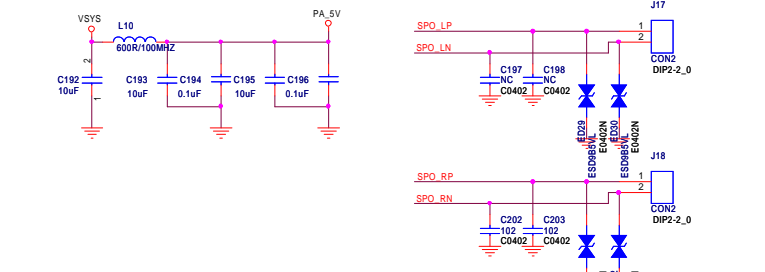
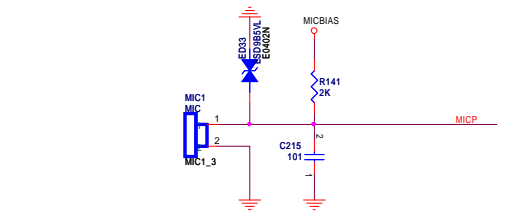
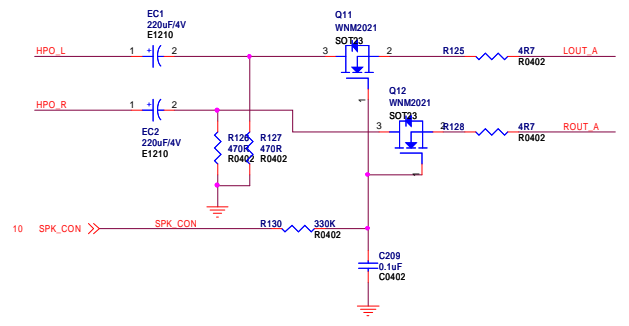


VGA OUT

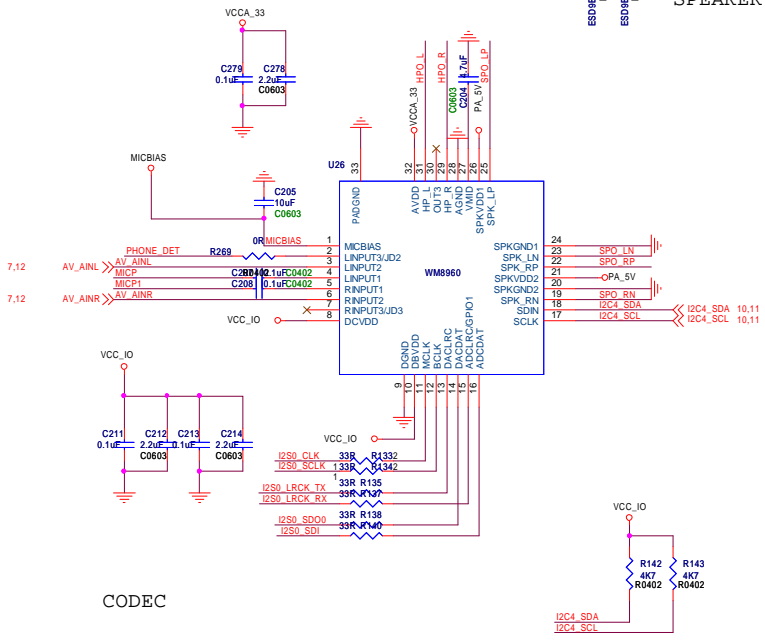
Rackchip 福州瑞芯微电子有限公司	
Title: YPbPr/CVBS/VGA OUT	
File: PX2_SDK_V10	REV: 1.1
Create Date: Friday, October 15, 2010	Page Num: 11
Modify Date: Thursday, June 12, 2014	Page Total: 17

U7L		
GPIO0_A7/I2S0_SDI	L21	I2S0_SDI
GPIO0_B0/I2S0_CLK	L22	I2S0_CLK
GPIO0_B1/I2S0_SCLK	R23	I2S0_SCLK
GPIO0_B2/I2S0_LRCK_RX	R22	I2S0_LRCK_RX
GPIO0_B3/I2S0_LRCK_TX	R21	I2S0_LRCK_TX
GPIO0_B4/I2S0_D0	K21	I2S0_SDO0
GPIO0_B5/I2S0_D1	D23	SATA_RESET 7
GPIO0_B6/I2S0_D2	N23	VGAORLCD_DET <<<VGAORLCD_DET 7,12
GPIO0_B7/I2S0_D3	M22	YCBCR_DET <<<YCBCR_DET 7,12
GPIO1_B2/SPDIF_TX	L22	SPDIF_TX

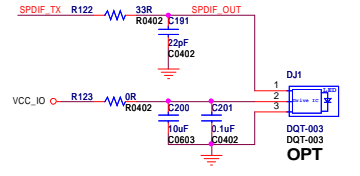
PX2-L



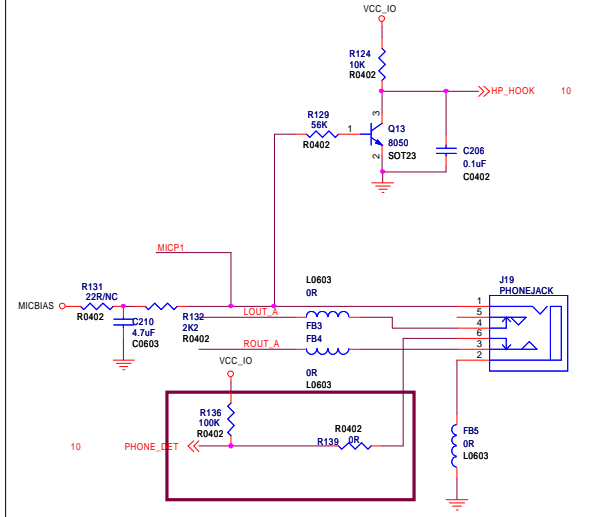
SPEAKER



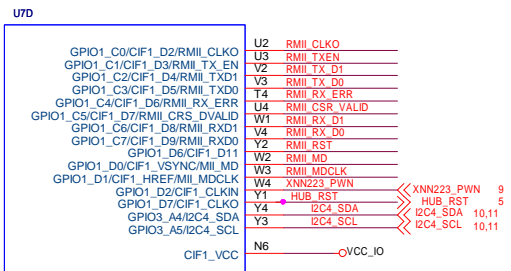
CODEC



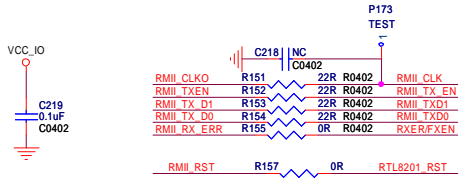
Optical SPDIF OUT



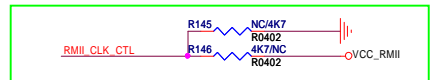
Rockchip 福州瑞芯微电子有限公司	
Title: SPDIF OUT/AUDIO	
File: PX2_SDK_V10	
REV: 1.1	
Create Date: Monday, May 07, 2012	Page Num: 12
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PX2-D

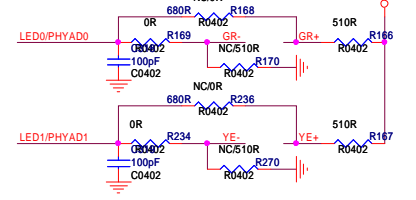


RMII REF_CLK direction



Pull Low for RMII REF_CLK Output mode
Pull High for RMII REF_CLK Input mode(default)

PHY Address/LED



MII/RMII Selection



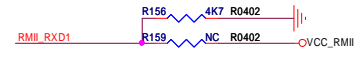
Pull High for RMII mode(default)

UTP / Fiber Selection

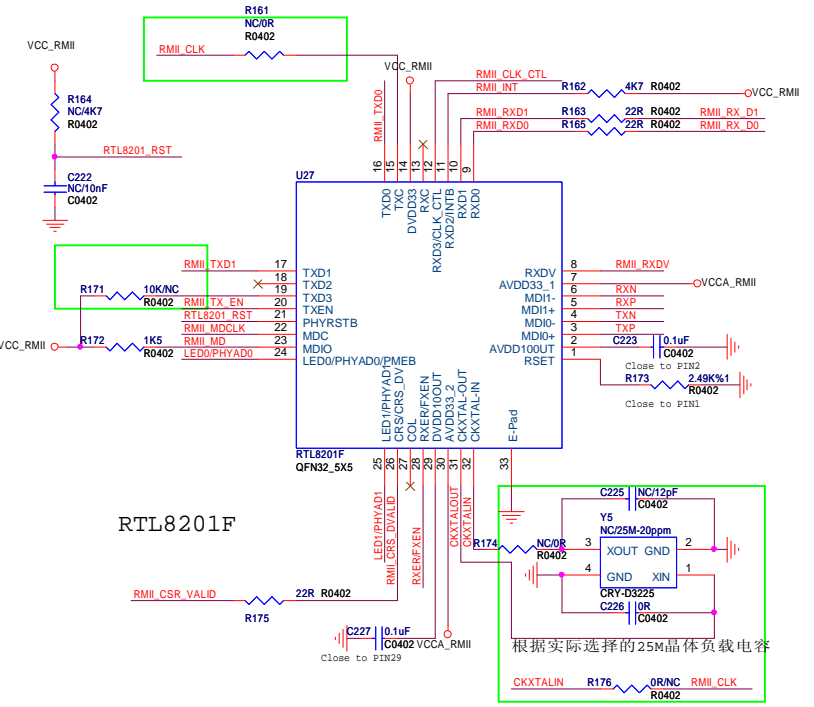


Pull Low for UTP Mode(default)
Pull High for Fiber Mode

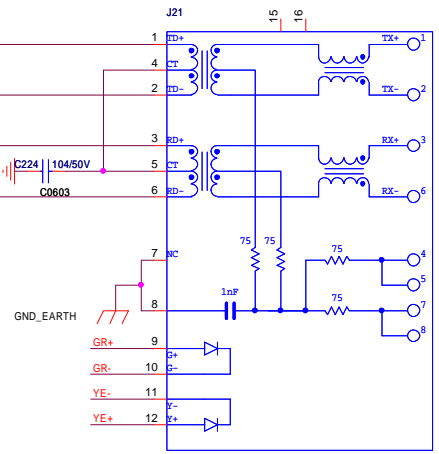
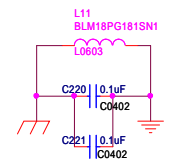
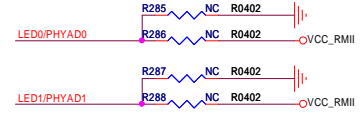
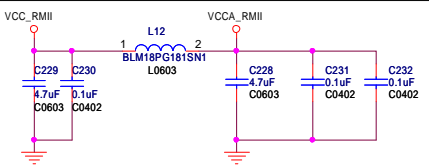
WOL/LED0 Selection



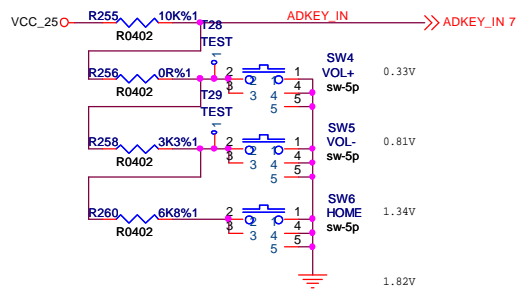
Pull Low for LED0 Mode(default)
Pull High for WOL Mode
WOL:Wake-on-LAN



RTL8201F POWER

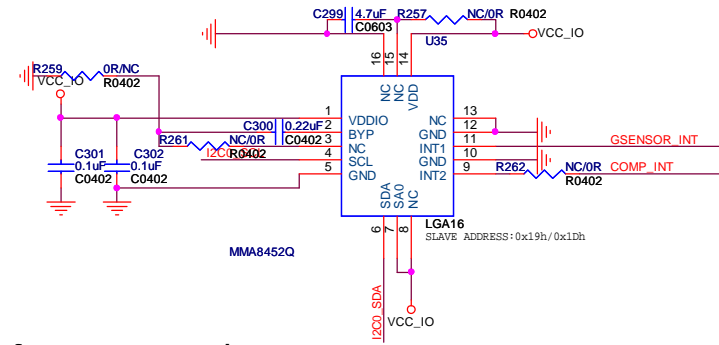


Rackchip 福州瑞芯微电子股份有限公司	
Title: RMII Ethernet	
File: PX2_SDK_V10	
REV: 1.1	
Create Date: Friday, October 15, 2010	Page Num: 13
Modify Date: Thursday, June 12, 2014	Page Total: 17

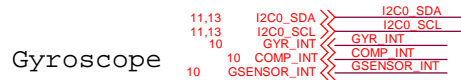
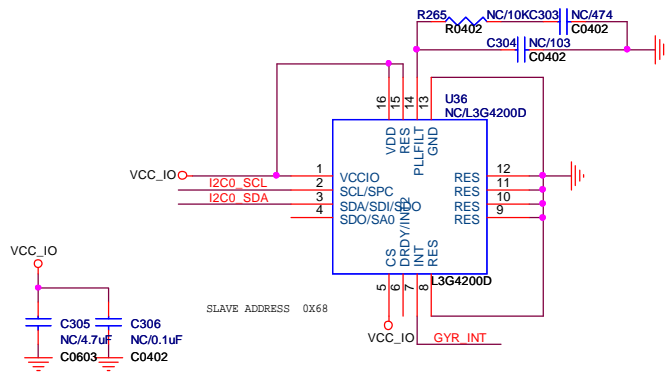


KEY BAORD

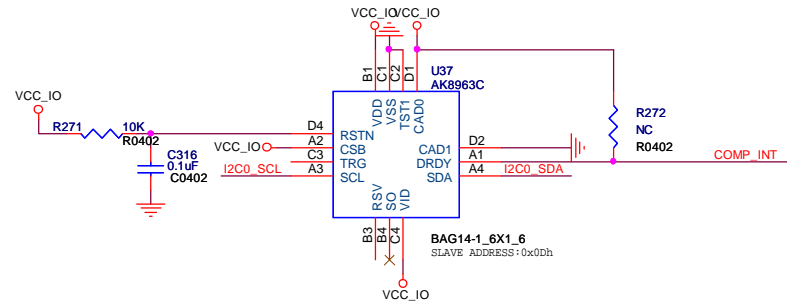
	LIS3DH/ MMA8452Q	LSM303D	KXT1K-1013
C303	NC	4.7uF	NC
R190	0ohm	NC	NC
R192	0ohm	NC	NC
C265	0.1uF	2.2uF	NC
U34	AK8963C	NC	NC
		R196 0R	
		R194 0R	



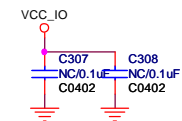
Note:
The first pin of G-sensor must be place on the lower left corner of PCB.
3D G-Sensor



Gyroscope



Compass



Title	<Title>	
Size	Document Number	Rev
B	<Doc>	<Rev Code>
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