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PCB POWER WIRE WIDTH INDICATE

	above 80 miles
	above 50 miles
	above 30 miles
	above 16 miles
No indicate	Under needs


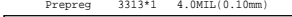



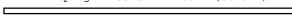


CM-RK3066

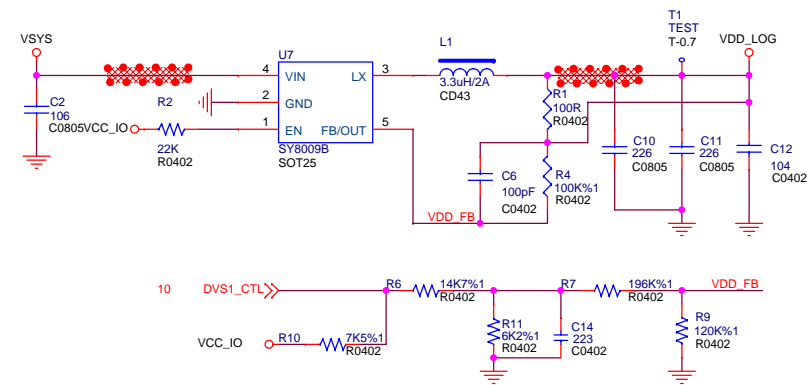
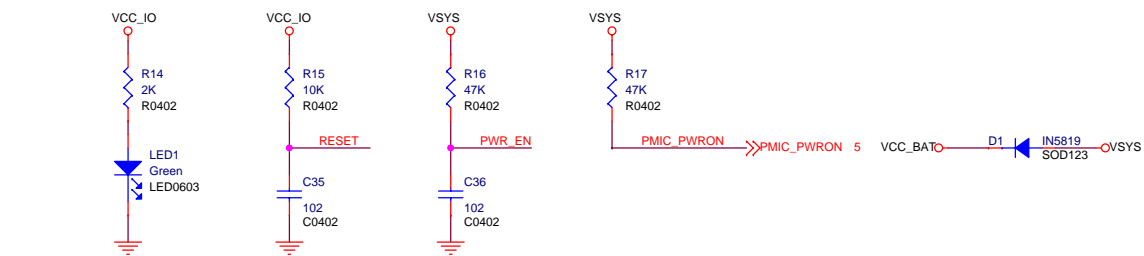
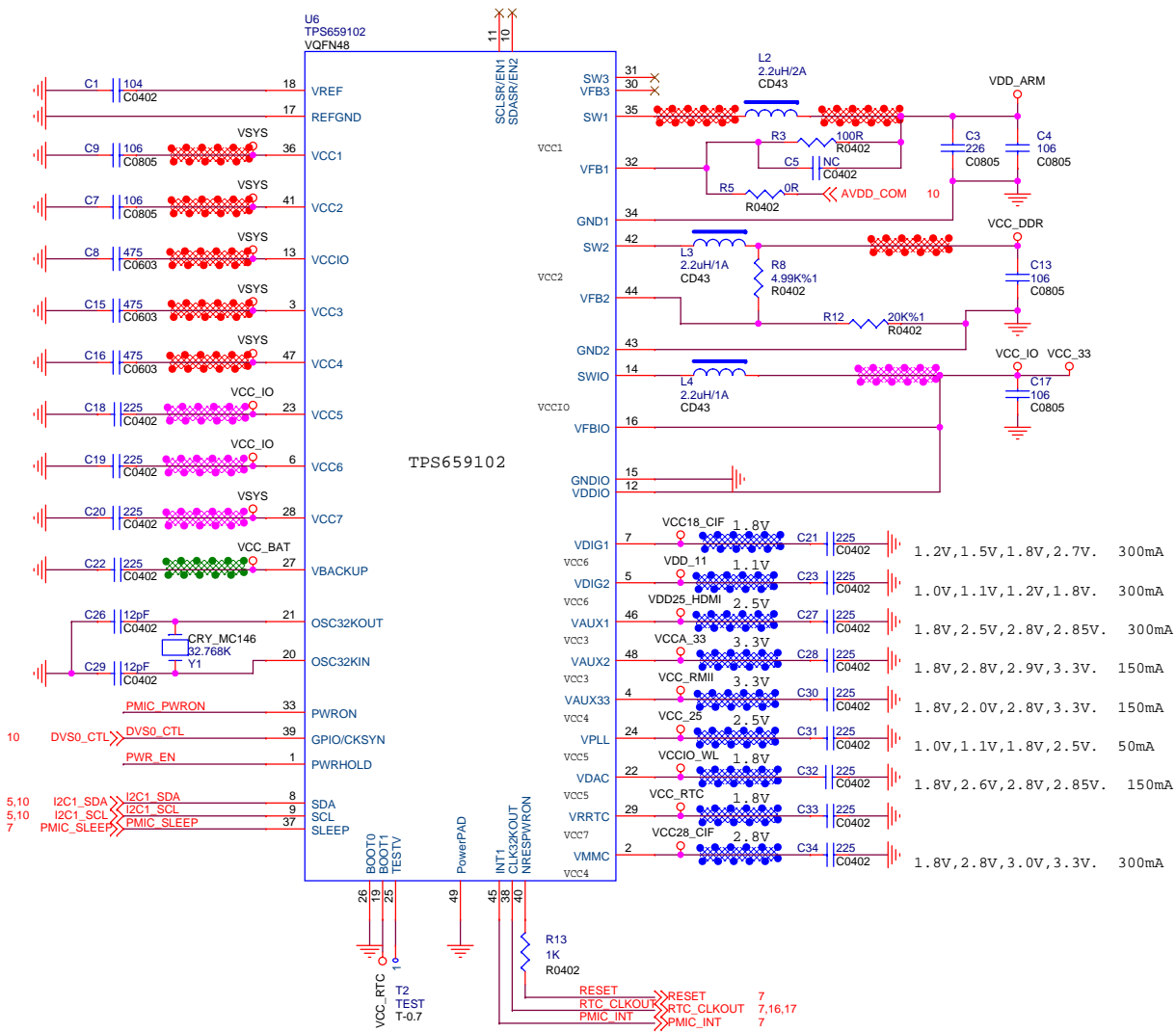
Designed by HAOYU Electronics

www.PowerMCU.com
www.MarsBoard.com

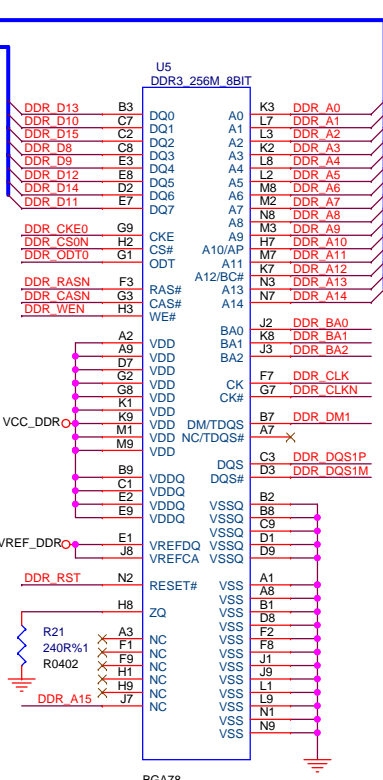
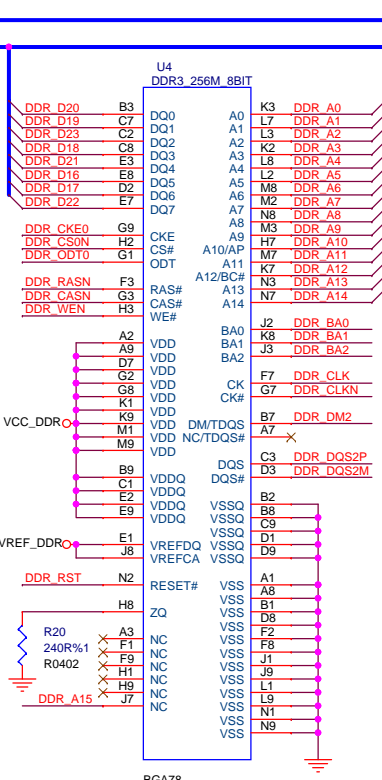
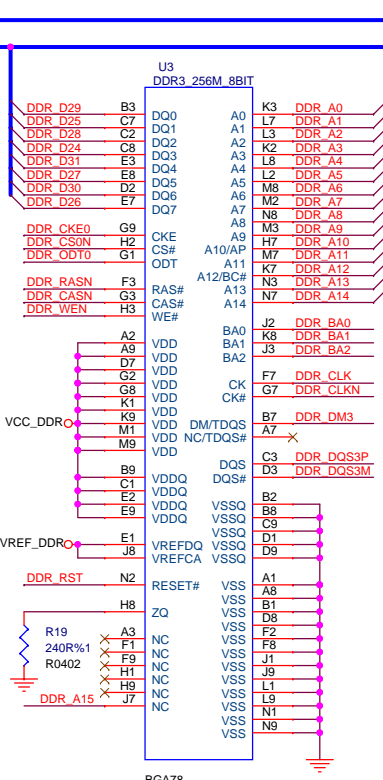
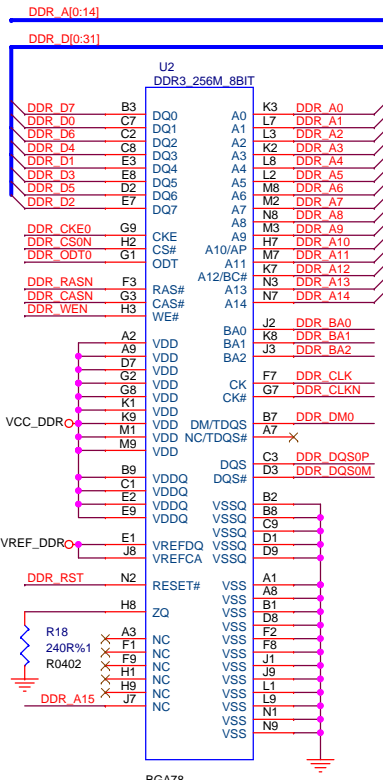
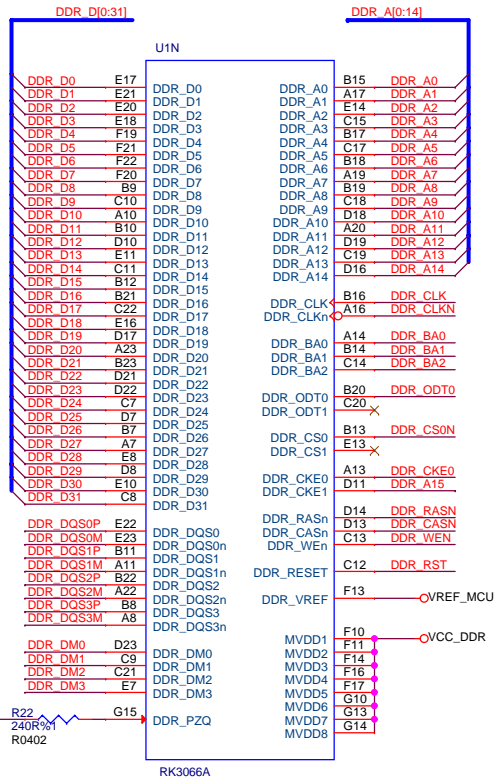
6 LAYERS PCB STACK

TOP		Hoz(18um) + plating copper(18um)
GND		1oz(35um)
POWER(S1)		1oz(35um)
	Adjust	
S1(S2)		1oz(35um)
GND(POWER)		1oz(35um)
S2(BOTTOM)		Hoz(18um) + plating copper(18um)

Version	Date	Author	Change Note	Approved
V1.0	20130826		First edictor	

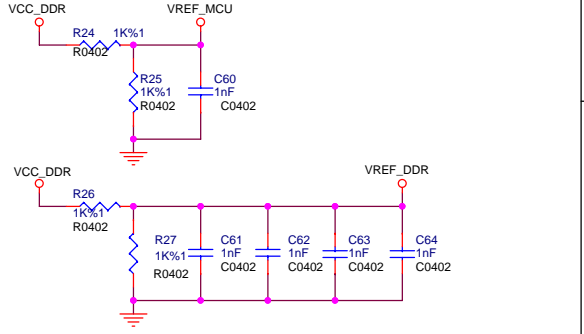
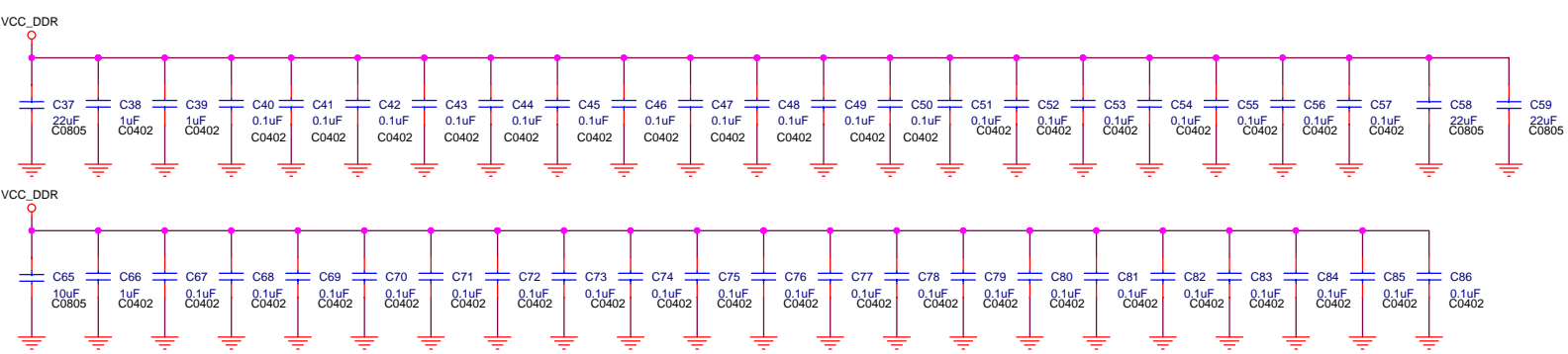


VDD_LOG POWER



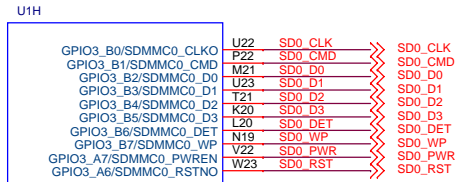
RK3066-N

DDR3



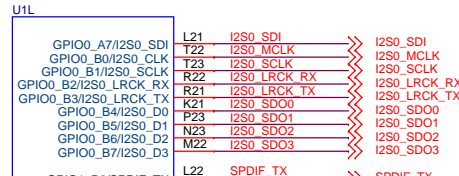
DDR FILTER

MarsBoard www.MarsBoard.com
Title: DDR3
File: CM-RK3066 REV: 1.0
Create Date: Wednesday, August 28, 2013 Page Num: 4
Modify Date: Friday, October 18, 2013 Page Total: 9



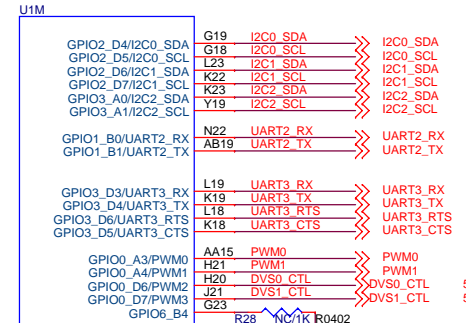
RK3066A

RK3066-H



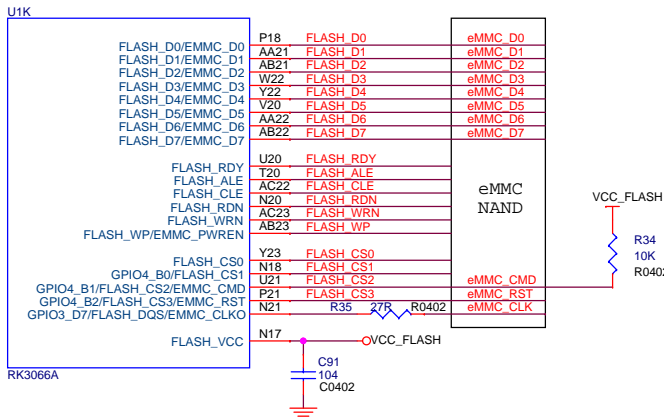
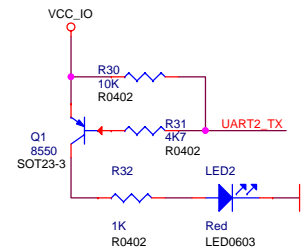
RK3066A

RK3066-L



RK3066A

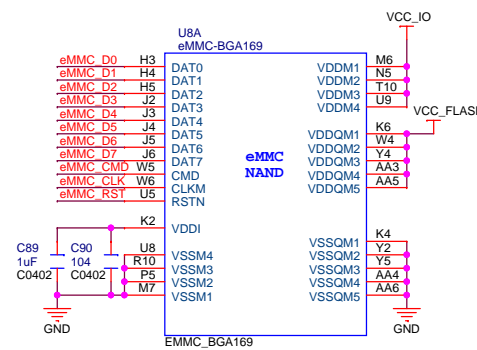
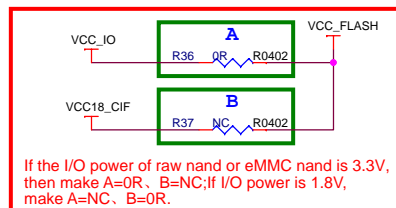
RK3066-M



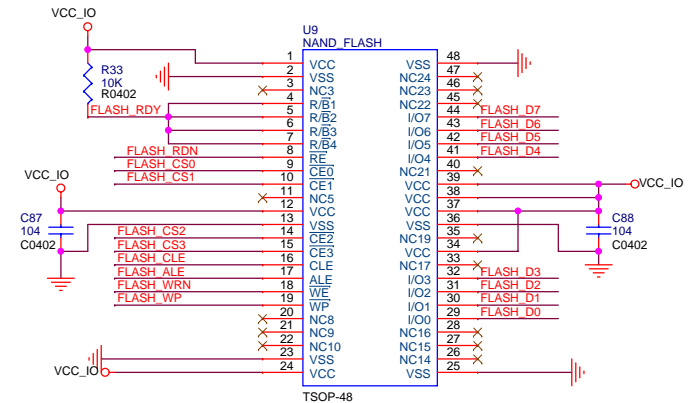
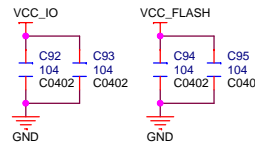
RK3066A

RK3066-K

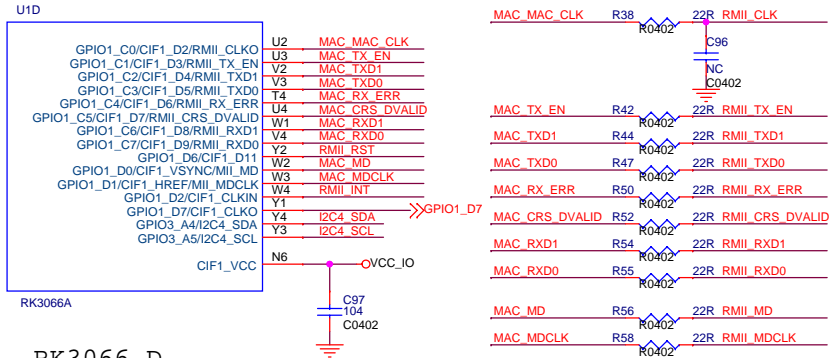
Default: A=0R; B=NC;



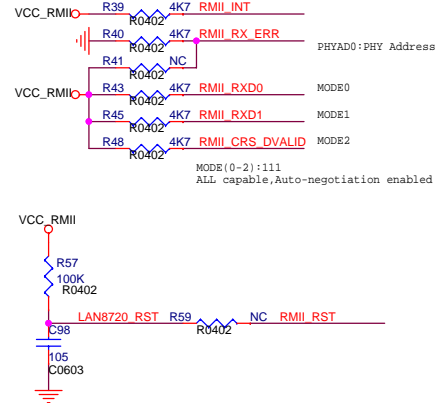
eMMC FLASH



NAND FLASH

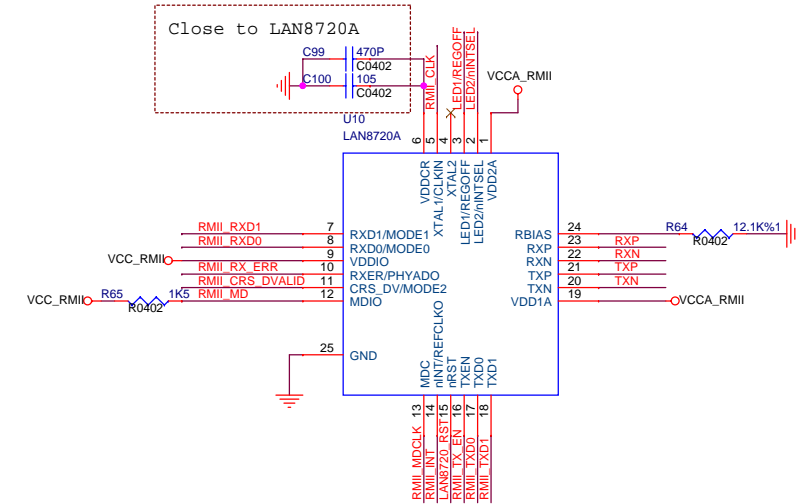
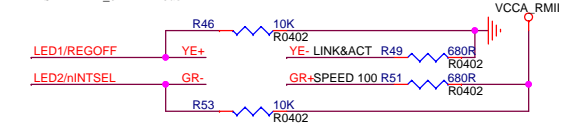


RK3066-D

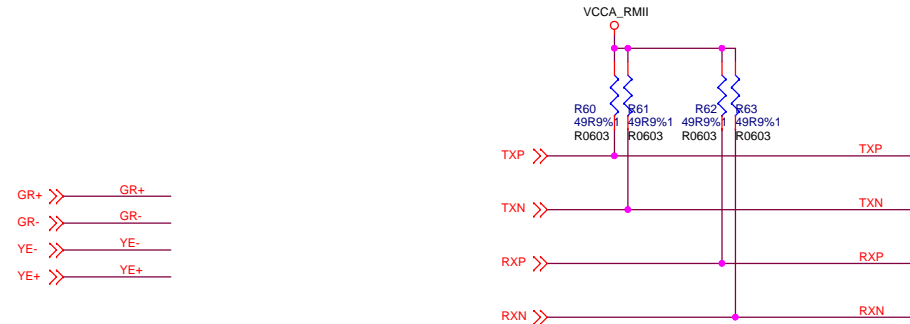


REGOFF=0:Internal +1.2V regulator ON

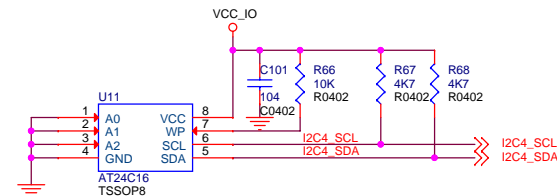
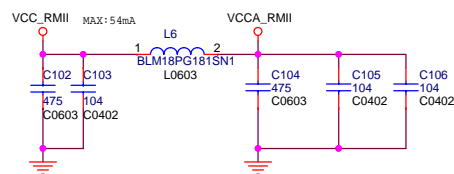
nINTSEL=1:REF_CLK In Mode



LAN8720A



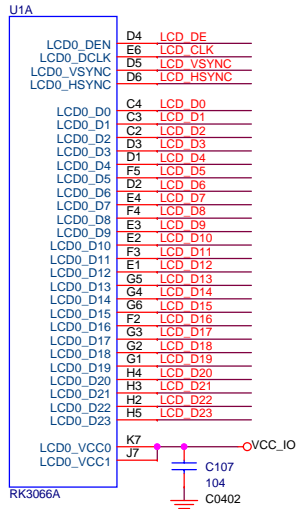
NOTE:
 Place 49.9 ohm
 resistors(0603)
 near LAN8720A.



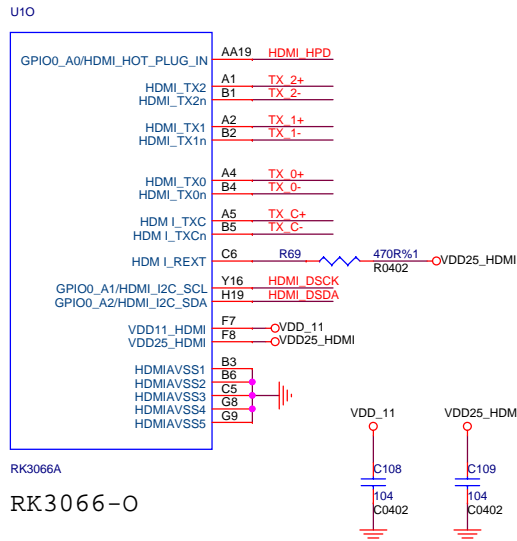
EEPROM :Ethernet MAC address

		www.MarsBoard.com
Title: RMII Ethernet		
File: CM-RK3066		REV: 1.0
Create Date: Friday, October 15, 2010	Page Num: 6	
Modify Date: Monday, December 23, 2013	Page Total: 9	

LAN8720A POWER

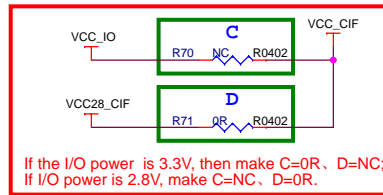


RK3066-A

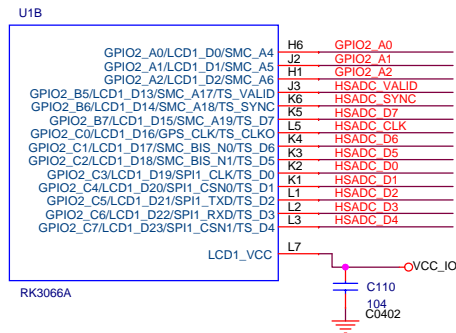


RK3066A
RK3066-0

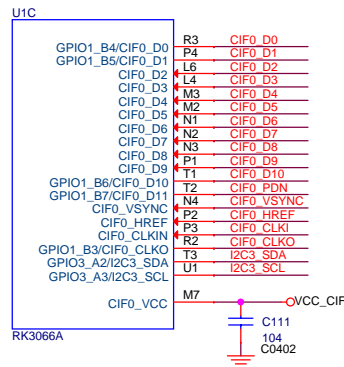
Default: C=NC; D=0R;



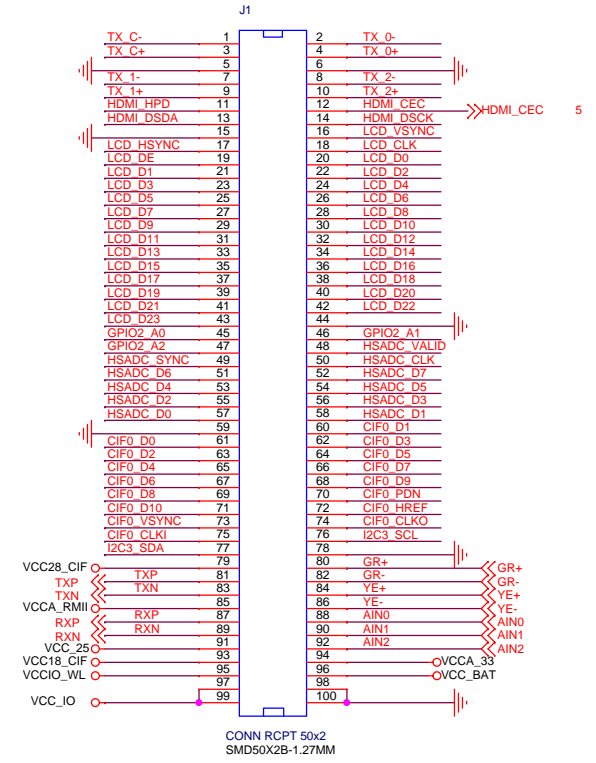
If the I/O power is 3.3V, then make C=0R, D=NC;
If I/O power is 2.8V, make C=NC, D=0R.

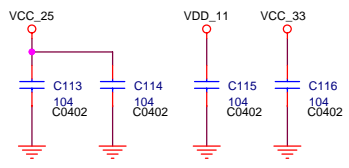
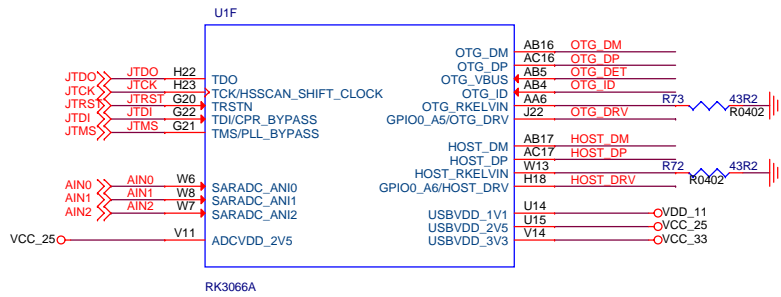


RK3066A
RK3066-B

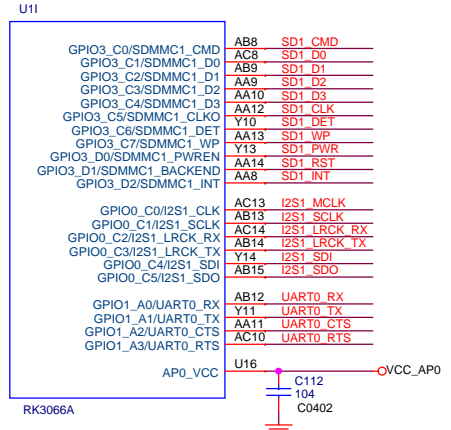


RK3066A
RK3066-C

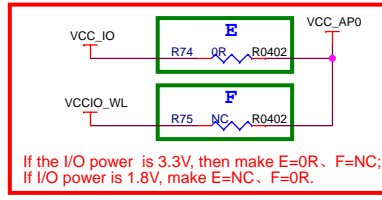




RK3066-F

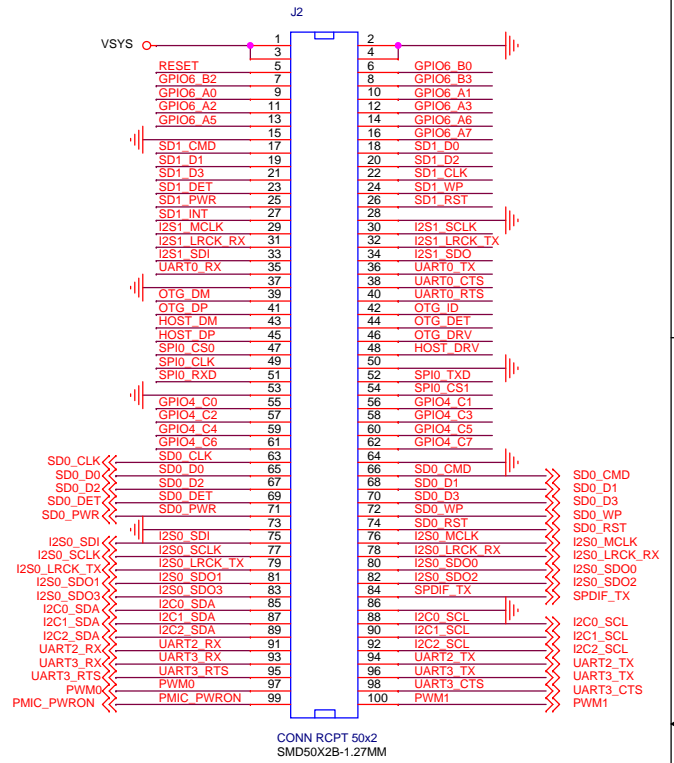


RK3066-I

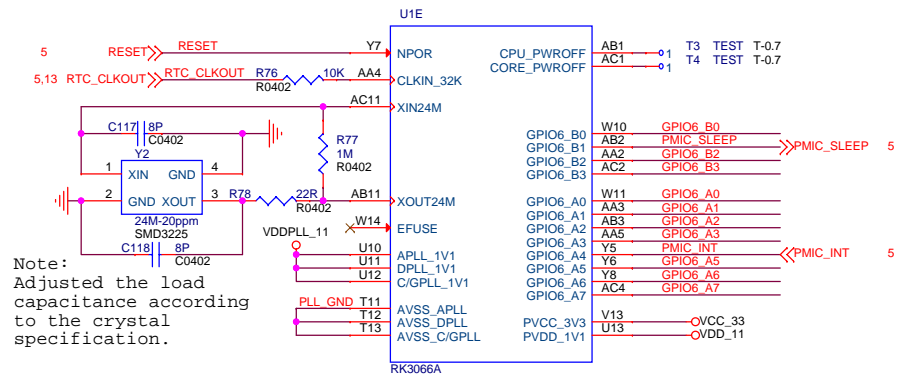


If the I/O power is 3.3V, then make E=0R; F=NC;
If I/O power is 1.8V, make E=NC; F=0R.

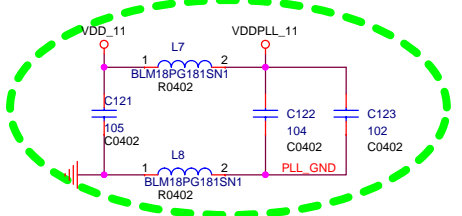
Default: E=0R; F=NC;



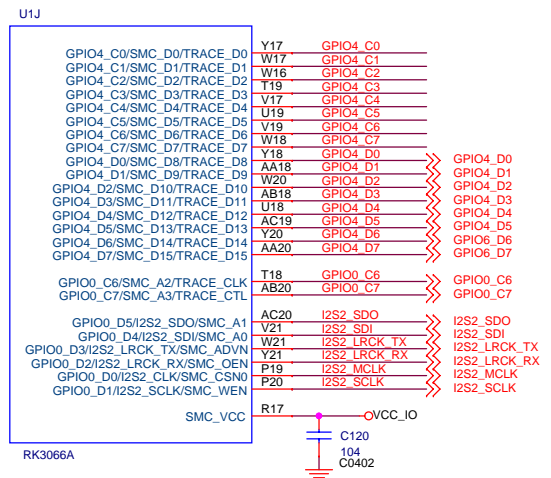
RK3066-G



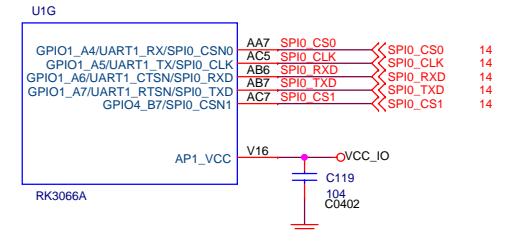
Note:
Adjusted the load capacitance according to the crystal specification.

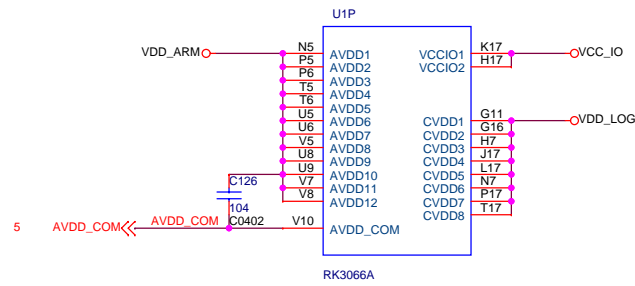
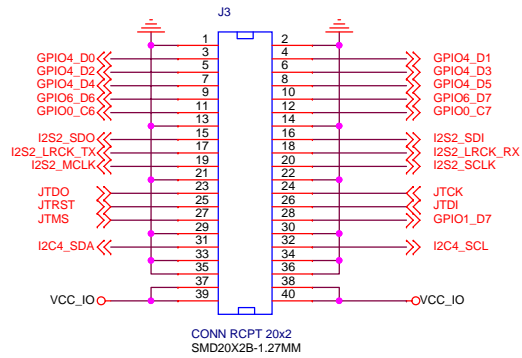


RK3066-E

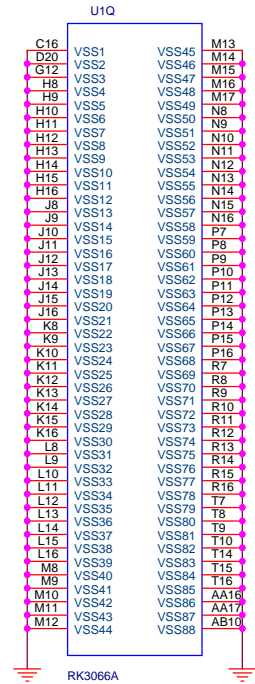


RK3066-J

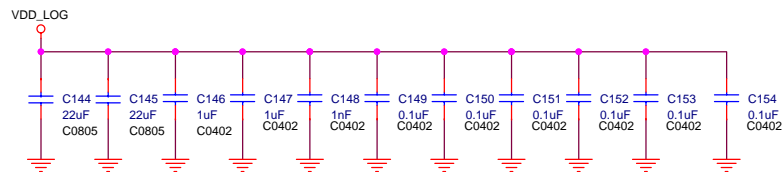
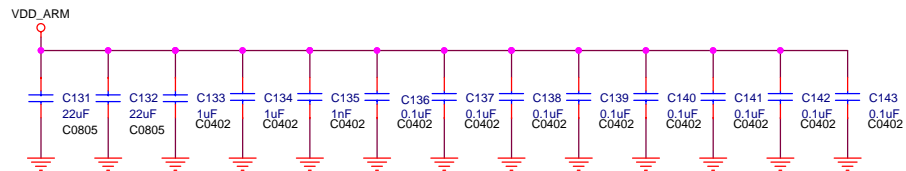
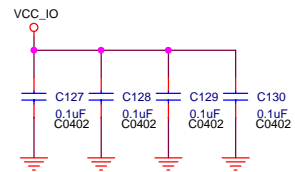




RK3066-P



RK3066-Q



RK3066 CORE POWER FILTER